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</tr>
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# CHAPTER 1

## GENERAL

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1. GENERAL

1-1. Specifications

SYSTEM CONFIGURATION
Standard Configuration:
Memory: CRT + Keyboard + Micro FDD
Floppy Disk: 256K
Disk: 3.5" single-sided, double-density, double-track
Capacity: 240K (text: 180K, about 80 pages)

DISPLAY
Screen Size and Type: 9" CRT
Color: Black-on-white
Format and Capacity: 24 lines by 80 characters (344 by 640 dot)
Underline Display: Standard
Centering Display: Standard
Cursor Position: LEAP, CREEP, SCROLL UP/DOWN

KEYBOARD
Number of Keys: 59 (U.S.A) to 61
Entry System: N-key rollover
Keytop: Concave, step sculpture

GENERAL FEATURES
Line Spacing: 1, 1-1/2, 2

FUNCTIONS
Cursor Movement
Leap Forward
Leap Backward
Leap Again
Local/Global Leap
Leap-Erase
Creep Forward
Creep Backward
Screen Scroll-Leap

Typing and Erasing
Automatic Page Break
Keyboard I and II
Shift Lock
Erase Forward
Erase Backward

Editing
Decimal Tab
Centering, margins, tabs
Automatic Reformatting
Block Move
Block Copy
Block Erase
Document Lock
Indent

SOFTWARE
FORTH

POWER SUPPLY
100, 120, 230V 50/60Hz

OPERATION ENVIRONMENT
Temperature: 10°C to 35°C (50°F to 95°F)
Humidity: 20% to 80%

DIMENSIONS
In millimeters: 342(W)×264(H)×510(D)
In inches: 13.5(W)×10.4(H)×20.1(D)

WEIGHT
9.2 Kg (20.3 lbs)

Subject to change without notice.
1-2. Overview

Fig. 1-1
System Overview

1.3 System Overview

Note: Only models for USA and Japan have modular jacks. Since other models do not have the internal modular jack, they do not have these jacks.
1-5. Block Diagram Description

<table>
<thead>
<tr>
<th>Block</th>
<th>LSI Name</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU/ROM/RAM</td>
<td>CPU MC68000</td>
<td>16 bit microprocessor&lt;br&gt;CPU Clock: 5 MHz&lt;br&gt;Address bus: A1 ~ A23 (A19 and A20 not used)&lt;br&gt;Data bus: D0 ~ D15&lt;br&gt;The CPU controls the entire system. It responds to the following two interrupts:&lt;br&gt;1. NMI: CPU operation is monitored by gate array #2. If the CPU operates abnormally, gate array #2 resets the entire system.&lt;br&gt;2. IPL0: Inputs IRQ from DUART. (For more information, see the DUART item.)&lt;br&gt;If a key input is not received within a certain time, the CPU saves the contents of the system RAM to disk and turns off the screen. (When a key is pressed, the screen is turned on and text is displayed.)</td>
</tr>
<tr>
<td>System ROM</td>
<td>The contents of the system ROM (256 KBYTES) are shown below.&lt;br&gt;1. System Program&lt;br&gt;2. Self Diagnosis Program&lt;br&gt;3. Keyboard code table/CG</td>
<td></td>
</tr>
<tr>
<td>SV-ROM</td>
<td>The SV-ROM is provided for the spelling check. When the spelling check is performed, it functions as a dictionary. (The SV-ROM is not available for some countries.)&lt;br&gt;The names of the dictionaries used and the availability of the SV-ROM is shown below.</td>
<td></td>
</tr>
</tbody>
</table>

1. ROM<br>○: Yes

<table>
<thead>
<tr>
<th>PARTS NO.</th>
<th>IC6</th>
<th>IC7</th>
<th>IC8</th>
<th>COUNTRY</th>
</tr>
</thead>
<tbody>
<tr>
<td>NH7-0684</td>
<td>○</td>
<td>—</td>
<td>—</td>
<td>USA, JAPAN</td>
</tr>
<tr>
<td>NH7-0724</td>
<td>○</td>
<td>—</td>
<td>—</td>
<td>U.K., OCEANIA</td>
</tr>
<tr>
<td>NH7-0813</td>
<td>○</td>
<td>—</td>
<td>—</td>
<td>QUEBEC, FRANCE</td>
</tr>
<tr>
<td>NH7-0814</td>
<td>—</td>
<td>○</td>
<td>—</td>
<td></td>
</tr>
<tr>
<td>NH7-1019</td>
<td>○</td>
<td>—</td>
<td>—</td>
<td></td>
</tr>
<tr>
<td>NH7-1020</td>
<td>—</td>
<td>○</td>
<td>—</td>
<td>W. GERMANY</td>
</tr>
<tr>
<td>NH7-1021</td>
<td>—</td>
<td>—</td>
<td>○</td>
<td></td>
</tr>
</tbody>
</table>

2. Dictionary name

<table>
<thead>
<tr>
<th>DICTIONARY</th>
<th>COUNTRY</th>
</tr>
</thead>
<tbody>
<tr>
<td>American Heritage</td>
<td>USA, JAPAN</td>
</tr>
<tr>
<td>American Heritage (British)</td>
<td>U.K., OCEANIA</td>
</tr>
<tr>
<td>Librairie Larousse</td>
<td>QUEBEC, FRANCE</td>
</tr>
<tr>
<td>Langenscheidt</td>
<td>W. GERMANY</td>
</tr>
</tbody>
</table>
1-5. Block Diagram Description

<table>
<thead>
<tr>
<th>Block</th>
<th>LSI Name</th>
<th>Function</th>
</tr>
</thead>
</table>
| CPU/ROM/RAM            | SV-RAM   | The SV-RAM (8 KBytes) is used to store a personal dictionary and the setup screen information. This personal dictionary and setup screen information are backed-up by a lithium battery.  
Personal dictionary: Dictionary registered by the user. It is used in the spelling check.  
Setup screen: Includes the setup data for the printer, modem, and other devices. |
| Memory Protect Circuit |          | This circuit protects the contents of the SV-RAM when the power is turned on and off and when the +5V supply is unstable.                |
| Gate Array #1 & #2/RAM | System & Video RAM | The system & video RAM consists of eight 4-bit D-RAMs. (256 KBytes standard) An optional RAM (128 KBytes) can be added. These RAMs are for video and system use.  
1. Video RAM: Stores the CG-pattern for CRT display.  
2. System RAM: Used to store the user Text, or for system operation. |
| Gate Array #1          |          | Gate Array #1 has the following functions:  
1. CPU clock generator  
   Divides a clock signal of approximately 20 MHz by four and outputs a 5 MHz CPU clock signal.  
2. CRT sync signal generation  
   HSYNC and VSYNC output.  
3. Memory control signal generation  
   Outputs the memory address, RAS, CAS, and WE signals and controls the system & video RAM. Also shows if the data output from memory is video data or CPU data.  
   Outputs the CS V/C signal to Gate Array #2. |
### 1-5. Block Diagram Description

<table>
<thead>
<tr>
<th>Block</th>
<th>LSI Name</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>Gate Array #1 &amp; #2/RAM</td>
<td>Gate Array #2</td>
<td>Gate Array #2 has the following functions:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>1. Decoder</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Outputs the I/O LSI and memory IC decode signals.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2. Video signal generation</td>
</tr>
<tr>
<td></td>
<td></td>
<td>The CS V/C signal multiplexes the read data from memory with the CPU data and video data. The video data is then converted by the LDPS signal from parallel to serial data and output to the CRT unit.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3. Relay control signal</td>
</tr>
<tr>
<td></td>
<td></td>
<td>The relays at the NCU are controlled as follows:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>① After a telephone ring is detected, the telephone line is connected to the main unit circuit by the OFF HOOK signal. (The line and telephone are still connected.)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>② Next, the telephone and telephone line are disconnected by the Phone signal. (Only the telephone line is connected.)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4. Reset signal delay control</td>
</tr>
<tr>
<td></td>
<td></td>
<td>RESIN signal is output after a delay of 0.7 ms.</td>
</tr>
<tr>
<td>CENTRO &amp; RS-232C</td>
<td>Gate Array #3</td>
<td>Gate Array #3 has the following functions:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>1. CENTRONICS printer interface</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2. Keyboard interface</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(1) The CPU stores key data by scanning the keyboard at 6.5 ms intervals.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>The DUART produces the key scan interval time.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(2) CC: Check code strobe</td>
</tr>
<tr>
<td></td>
<td></td>
<td>After the power switch is turned on, the signal level is lowered and the status of bit D7 is checked. If it is on, self diagnosis is performed.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3. Low Battery Detector</td>
</tr>
<tr>
<td></td>
<td></td>
<td>The Reset IC monitors the lithium battery, and when low BAT = high, a battery mark is displayed on the ruler line.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4. FDD Controller</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(1) Outputs the signal which drives the FDD and reads the FDD status.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(2) Separates the read data from the FDD into clock bits and data bits.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Note:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>The CPU controls data transfer between the FDD and system RAM.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>At this time, the CPU inhibits all interruptions.</td>
</tr>
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1-5. Block Diagram Description

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<th>Block</th>
<th>LSI Name</th>
<th>Function</th>
</tr>
</thead>
</table>
| CENTRO & RS-232C | DUART     | The DUART has the following functions:  
1. The DUART has two ports. One port is for the internal modem and the other port is for the external modem or printer. (External modem or printer is selected by setup screen.)  
2. Buzzer control  
3. The DUART outputs IRQ to the CPU under the following two conditions:  
   ① When the CPU key-scan timing signal is generated by the internal counter of Gate Array #2. TOBF is output to DUART every 6.5 ms. and DUART outputs IRQ to the CPU.  
   ② When a telephone ring is automatically detected by the main unit... |

<table>
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<tr>
<th>Modem &amp; NCU</th>
<th>Modem Chip</th>
<th>This IC is a single chip modem which complies with Bell 212A and CCITT V.22 standards. It has a 1200bps PSK modem and fall back mode 300bps FSL modem functions.</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Modem Filter</td>
<td>The filter specifications conform to the Bell 212A and CCITT V.22 standards.</td>
</tr>
<tr>
<td></td>
<td>DTMF Tone Generator</td>
<td>This LSI outputs a dial tone to the telephone line without the use of a telephone set when automatic dialing is performed.</td>
</tr>
</tbody>
</table>

These LSIs and circuits are installed in models with internal modems. Only the U.S.A, ASIA, QUEBEC, OCEANIA, and JAPAN have modem models with internal specifications.  
External modem specifications apply to other countries.
1-6. Connector Layout

Fig. 1-4
CHAPTER 2

THEORY
2. THEORY

2-1. Memory Map

Address

$ FFFFFFF

$ E00000

$ C00000

$ A00000

$ 800000

$ 600000

$ 480000

$ 400000

$ 280000

$ 200000

$ 042000

$ 040000

$ 000000

$ 800000

$ 870000

$ 860000

$ 850000

$ 840000

$ 830000

$ 820000

$ 810000

$ 800000

$ 480000

$ 460000

$ 440000

$ 420000

$ 400000

$ 280000

$ 240000

$ 200000

$ A00000

$ 880000

$ 870000

$ 860000

$ 850000

$ 840000

$ 830000

$ 820000

$ 810000

$ 800000

$ 480000

$ 460000

$ 440000

$ 420000

$ 400000

$ 280000

$ 240000

$ 200000
2-2. Initial Flowchart

1. Flowchart

```
START

1. Checks the system & video RAM size

2. Initializes the Gate Array #1

3. Initializes the DUART.

4. Initializes the Gate Array #3 and #2.

5. Initializes the Modem chip.

6. Disables the CS pin of DTMF tone.

7. Reads the test SW status.

   Test Mode?

   Yes  To self-diagnosis routine

   No

8. Initializes the system RAM.

9. Starts key scan.

10. Reads the country code.

11. Passes control to the FORTH interpreter.

12. Checks and recover SVRAM.

   OK?

   Yes  Recovers the SVRAM data.

   No  Editing Screen
```
2-2. Initial Flowchart

2. Flowchart Description

1. There are 512K bytes of space allocated for the System & Video RAM. However, this machine only uses a maximum of 384K bytes for the System & Video RAM. An R/W check is done for every 128 K bytes of this area, and the RAM size is checked. (Only addresses $400000, $420000, $440000, and $460000 are checked.)

2. Gate Array #1 has a register for control of the CRT. Initialize the screen size, H and V SYNC signals, and screen off registers.

3. DUART has two kinds of registers for control of two RS-port. Initializes these registers.

4. Gate Array #3 has one register for control of the drive R/W and one keyboard key strobe register. Initializes these registers.

5. Gate Array #2 has registers for control of the timer, video signals and etc. Turns off Output Enable for the Video signal and turns off the Off Hook signal.

6. The modem chip operates on the basis of 29 commands. Sets the phase synchronization for the internal PLL circuit and set the pin state of modem chip. (The TxD and RxD pins are high impedance.)

7. Disables the CS signal for DTMF tone.

8. Reads the test switch status. If D7 is low, jump to the self-diagnosis routine.

9. Initializes the system & Video RAM.

10. Starts the key scan.

11. Reads the country code.

12. Enters the FORTH execution mode. FORTH performs initialization.

13. Setup screen information and the user dictionary are stored in SV-RAM. A sum check is performed for the setup screen information. If the values are unequal, the default value is set according to the country code.

The user dictionary is checked in the same way. If an error is detected, the dictionary is cleared.

14. Displays the editing screen.
2-3. Circuit Description

1. CPU PCB Unit

(1) Operation Outline
Operation of CPU and peripheral equipment after initialization is shown below. (Refer to the block diagram description for an outline of each element)

(1) When a key is input:
CPU gets and processes keydata at intervals of 6.5 ms.

①-1. The Gate Array #2 has a timer to count the key strobe interval time.
①-2. The KTOBF signal is outputted to the DUART when the count is full count. (The KTOBF: 6.5 ms period)
①-3. Upon detection of the KTOBF signal, the DUART outputs IRQ signal (6.5 ms period) to the CPU.
①-4. After receiving the IRQ, the CPU outputs keystrobe data to Gate Array #3 to get key codes from the keyboard.
①-5. CPU arranges the codes with a keytable according to the country code and convert them into the system code (like ASCII code).
①-6. CPU stores the codes into the text area in the SYS & video RAM, reads their character-pattern from the CG area in SYS ROM, and saves it into the video area into the SYS & video RAM.
①-7. Read and write timing for these data are controlled by Gate Array #1.
①-8. Gate Array #2 reads the video data and converts it from parallel to serial with the LDPS signal and outputs the video data to the CRT.

(2) When you set the setup screen:
The CPU stores two kinds of information in SV-RAM.

①. Setup information
②. Personal dictionary

Whenever write the data, the CPU checks PFAIL bit. If PFAIL bit is on, the CPU stops to write the SV-RAM’s data. If it is off, the CPU writes it into the SV-RAM. (PFAIL bit is set when the +5V is instable.)

(3) Text save
FDC is built in the Gate Array #3. Read/Write of μFDD is controlled by this LSI. When saving Text on the disk, the CPU saves system RAM data (includes text) and SV-RAM data on the disk. The data is saved separately in the disk text area and SV-RAM area.

(4) Text call
④-1. Text data and SV-RAM data are loaded together if user set load setup or spelling from the disk at the setup screen when loading Text data from disk to System RAM.

④-2. The read data signal from the μFDD is separated into its components signals, clock bit and data by Gate Array #3.

(5) When you leave from keyboard:
If the screen blanking time is not set to “Never” in the setup screen, text data is automatically saved to disk by stopping key input for the established time.

If CPU doesn’t get key data for three consecutive scans, the screen-off count begins from this point. If any key input is indicated before count end, the counter is cleared.

If count is full count, the CPU inhibits all interruptions and text data is transferred to FDD. Then the video signal goes high and the picture is turned off (The system and video RAM information remains as is. In this state, the Video signal does not function but other signals continue to operate.)
2-3. Circuit Description

⑥ For communications (The example is for Automatic answering.)

⑥-1. The telephone set is connected with an exchange through CN7 and CN6 during standby.
In this case, DC-48V voltage is applied between 3 and 4 pin of CN6.

⑥-2. The calling indicator signal is input to the telephone set from the exchange. The signal
also makes the telephone set ring.
(Calling indicator signal: 16 Hz, 75 V ring tone signal)

⑥-3. DC element is removed from the calling indicator signal by C57, and only the AC signal
is applied to PC1.
The signal then appears on the RG-line.

⑥-4. The RG signal is connected to DUART pin 2.
The level is periodically checked by the CPU. If there are variations in the signal level,
the CPU counts the number of low levels (the number of rings).
When this value becomes equal to the number of rings, auto answering is done.

⑥-5. Turn on the OFF-Hook signal and switch relay K2 to the NCU side. (For auto answering,
leave relay K1 as is.
For manual answering, turn on the phone signal and disconnect the telephone set line
from the mainframe circuit.)

⑥-6. A DC-current loop is then established between the exchange and T1 (1-2).

⑥-7. The DC element is removed by T1 and the carrier is transferred between 4 and 3. It
is then converted by R38 and input to Filter IC38.

⑥-8. IC38 has two Op Amps. One is for the sending carrier, and one is for the receiving carrier.
This IC splits the receiving carrier from the sending carrier, amplifies it, and inputs it
to RxIN.

⑥-9. IC38 also has two filters. The modem chip sets the answer mode for these filters. (The
low filter is used for receiving; the high filter is used for sending.)
The carrier is output from RxOUT via the low filter.

⑥-10. The modem chip judges whether the carrier is FSK or PSK. The CPU reads the modem
chip status to obtain the result of this judgement.

⑥-11. The carrier is then demodulated by the modem, and the serial data is output to DUART.

⑥-12. DUART converts the serial data from serial to parallel.
The data is then stored in the system & Video RAM.
2-3. Circuit Description

(2) Gate Array #1

① Function description
(Refer to Page 1-6)

② Block diagram

③ Signal description

<table>
<thead>
<tr>
<th>Signal name</th>
<th>I/O</th>
<th>Signal description</th>
</tr>
</thead>
<tbody>
<tr>
<td>HSYNC</td>
<td>O</td>
<td>CRT horizontal synchronizing signal. Approx. 24 KHz.</td>
</tr>
<tr>
<td>VSYNC</td>
<td>O</td>
<td>CRT vertical synchronizing signal. Approx. 60 Hz</td>
</tr>
<tr>
<td>RAM/RAM.CS</td>
<td>O</td>
<td>Signal enable memory decoder of Gate Array #2</td>
</tr>
<tr>
<td>MODE</td>
<td>I</td>
<td>This terminal is for manufacture (always low)</td>
</tr>
<tr>
<td>CPU CLK</td>
<td>O</td>
<td>CPU Clock 5 MHz (200 ns)</td>
</tr>
<tr>
<td>20 M</td>
<td>O</td>
<td>Timing of RAS, CAS etc. Used for the shift clock of video data.</td>
</tr>
<tr>
<td>2.45 M</td>
<td>O</td>
<td>Not used</td>
</tr>
<tr>
<td>3.68 M</td>
<td>O</td>
<td>Clock for DUART operation</td>
</tr>
<tr>
<td>CS. V/Č</td>
<td>O</td>
<td>CPU basic memory cycle time is 800 ns. One cycle consists of a CPU read/write cycle</td>
</tr>
<tr>
<td>LDPS</td>
<td>O</td>
<td>Loading video data and converts it from parallel to serial.</td>
</tr>
<tr>
<td>I/O CS</td>
<td>O</td>
<td>Signal enable I/O decoder of Gate Array #2.</td>
</tr>
</tbody>
</table>
2-3. Circuit Description

(2) Gate Array #2

① Function description
(Refer to Page 1-7)

② Block diagram

<table>
<thead>
<tr>
<th>Signal name</th>
<th>I/O</th>
<th>Signal description</th>
</tr>
</thead>
<tbody>
<tr>
<td>MNI</td>
<td>O</td>
<td>Signal resets the system when CPU is abnormal. Gate Array #2 has a watch dog timer.</td>
</tr>
<tr>
<td>RESIN</td>
<td>I</td>
<td>After the signal from the RESET circuit is input, the RESET signal is output with</td>
</tr>
<tr>
<td>RESET</td>
<td>O</td>
<td>a 0.7ms delay.</td>
</tr>
<tr>
<td>KTOBF</td>
<td>O</td>
<td>One of the timers produces the keystrobe interval time (6.5ms). When the counter</td>
</tr>
<tr>
<td>OFF HOOK PHONE</td>
<td>O</td>
<td>indicates count end, KTOBF = High is output to DUART. This signal is used as a key</td>
</tr>
<tr>
<td></td>
<td></td>
<td>interrupt signal to CPU through DUART.</td>
</tr>
</tbody>
</table>

① When the mainframe is set to automatic reception, Ring is detected. OFF HOOK=High is set, K2 relay is set on, and the telephone circuit is connected with the mainframe.

② Next, PHONE=High is set, K1 relay is set on and the telephone set is separated from the telephone circuit.

③ When auto dialing with a dial-phone, the dial pulse is generated by switching the OFF HOOK signal on and off.
2-3. Circuit Description

(4) Gate Array #3

1. Function description
   (Refer to Page 1-7)

2. Block diagram

<table>
<thead>
<tr>
<th>Signal name</th>
<th>I/O</th>
<th>Signal description</th>
</tr>
</thead>
<tbody>
<tr>
<td>PE</td>
<td>I</td>
<td>When there is no paper in the printer, the signal goes High.</td>
</tr>
<tr>
<td>IPP</td>
<td>O</td>
<td>Signal initializes the printer. This signal synchronizes the mainframe with the printer initialization.</td>
</tr>
<tr>
<td>ERR</td>
<td>I</td>
<td>The signal goes Low when there is an error in the printer.</td>
</tr>
<tr>
<td>DSTB</td>
<td>O</td>
<td>Centro data strobe signal</td>
</tr>
<tr>
<td>LEDE</td>
<td>O</td>
<td>Signal switching for LED when a lock key is pressed.</td>
</tr>
<tr>
<td>C.C</td>
<td>O</td>
<td>After initialization, C.C = level is set to check whether the test switch is On. A self diagnosis test is executed when the test switch is On.</td>
</tr>
<tr>
<td>DSO</td>
<td>O</td>
<td>Drive select signal</td>
</tr>
</tbody>
</table>
2-3. Circuit Description

2. CRT/Power Supply Unit (MATSUSHITA)

(1) Power Supply Specifications

<table>
<thead>
<tr>
<th>Item</th>
<th>Voltage</th>
<th>Use</th>
</tr>
</thead>
<tbody>
<tr>
<td>VCC</td>
<td>+5 V ±4%</td>
<td>CPU PCB, KEYBOARD</td>
</tr>
<tr>
<td>+12 V</td>
<td>+12 V ±3%</td>
<td>CRT</td>
</tr>
<tr>
<td>−12 V</td>
<td>−12 V ±1V</td>
<td>CPU PCB (RS-Driver)</td>
</tr>
</tbody>
</table>

(2) Circuit Description (Power Circuit)

The power circuit consists of four blocks which are described below (Refer to circuit diagram).

① EMI filter & rectifier

This block rectifies the AC voltage and passes it through a filter to generate DC voltage. The functions of the main element are shown below.

1) R1 limits inrush current to C6, C7, C5, and C24 when the power is switched on.
2) When the input voltage changeover terminal is set to 115V, the input voltage is divided by C6 and C7. They also work to remove ripple.

② Main chopper

This block consists of transistors Q2, Q3 and transformer T2. When transistor Q2 (main chopper) is set on, energy is stored in transformer T2. When Q2 is set to off, the energy is converted into the secondary voltage. Thus, the primary voltage is converted into the secondary voltage while repeatedly turning Q2 on and off. The functions of main elements are shown below.

1) C8, R4 and D1 drive the base voltage of Q2.
   R2 is the starting resistor for switching on Q2.
2) D3 (Catch-diode) holds the fly-back voltage during Q2 off.
3) L3 and R6 improve the turn-off speed.

③ DC output

This block rectifies the high-frequency wave and smooths it with a filter to generate DC voltage. The functions of main elements are shown below.

1) D8 and D7 are diodes used for +5 V rectification. D6 is for +12 V rectification and D9 is for −12 V rectification.
2) The π (π) form filters C16, C17, C20, L6 remove the 5 V line ripple.
3) The π (π) form filters C15, L5, C801 remove the +12 V line ripple.
4) The π (π) form filters C18, C17, C19 remove the −12 V line ripple.
5) The overvoltage protective circuit (SCR1, Z1, R27) switches off Q2 and switches on SCR1 when the 5 V line voltage exceeds the voltage in Z1.

④ Voltage regulation & current limiter

This block monitors the output current with IC1. When the current exceeds the specified value, the block switches transistor Q3 on and Q1 off. This operation regulates the voltage and also controls the output current. The main functions are shown below.

1) D5, R15, C12, D11 and R14 are the Q3 current limiting circuits.
2) IC1 compares the 2.5 V reference voltage with the output voltage. If the output voltage exceeds the specification, Q3 is switched on.
3) L4 and R16 produce a saw tooth waveform in the Q3 editor when Q3 is switched on. It switches off Q1 and T3.
2-3. Circuit Description

(3) Circuit Description (CRT)

The CRT circuit consists of six blocks which are described below (Refer to circuit diagram).

1. Video circuit
   - Video input is inverted and amplified by the linear amplifier Q351 to drive CRT cathode
   - L352, C351 and R352 are inserted in collector emitter of Q351 to compensate high pass
     and have a flat frequency characteristic

2. CRT circuit
   - This emitted electrons driven by the cathode are controlled by G1, and accelerated by
     G2. They strikes against the CRT fluorescent material by supplying focusing voltage to
     G4, resulting in picture brightness regeneration.

3. Bright ADJ circuit
   - The brightness is controlled by adjusting the CRT G1 voltage obtained by changing R531
     and R530 connected between the positive and negative voltages obtained from the FBT.
   - Burning of the fluorescent surface of the CRT by the residual spot phenomenon when
     the power switch is turned off is prevented by the charging and discharging circuit made
     up of C519, R524, R518 and D510 connected in series with the G1 circuit as a spot killer.

4. Vertical deflection circuit
   - The vertical oscillator is turned on and off by the charging and discharging circuit made
     up of C403, R405 and R416 and has a 56 Hz vertical oscillation frequency synchronized
     to V.SYNC.
   - The amplitude of the sawtooth waveform generated at pin 8 is adjusted with R418.
   - Vertical scanning is performed by passing the sawtooth waveform current taken from
     the output circuit through deflection coil DY(V).
   - The vertical linearity is corrected by changing the feedback waveform with R417.

5. Horizontal output circuit
   - The horizontal oscillator is oscillated by the charging and discharging circuit made up
     of C506, C523, R508 and R528 and 24 KHz obtained by controlling the horizontal
     oscillation frequency automatically by AFC circuit.
   - The IC predrive circuit switches Q503 and drives the horizontal deflection and high
     voltage generation circuits.
   - The deflection circuit is connected in series yoke DY(H).
     Linearity is adjusted with L502 and linear horizontal deflection is performed by L503
     and C521.
   - The voltage needed at the CRT is obtained by boosting (T501) and rectifying the flyback
     pulse by Q503 switching operation.

6. Dynamic focus circuit
   - The approximate anode = 13 kV, G2 = 500 V, video = 50 V, and brightness circuit positive
     and negative voltages are obtained from the voltage taken from T501.
   - The G4 voltage is varied with R529 and a static focus voltage is obtained.
2-3. Circuit Description

3. CRT/Power Supply Unit (GOLD STAR)

(1) Ratings (Power Supply)

1) Input

<table>
<thead>
<tr>
<th>ITEMS (RATED VOLTAGE)</th>
<th>220/240V</th>
<th>100V</th>
<th>120V</th>
</tr>
</thead>
<tbody>
<tr>
<td>INPUT VOLTAGE</td>
<td>AC 230V +/−15%</td>
<td>AC 100V +32%/−10%</td>
<td>AC 120V +/−15%</td>
</tr>
<tr>
<td>FREQUENCY PROTECT</td>
<td>47 ~ 53 Hz</td>
<td>47 ~ 53 Hz, 57 ~ 63 Hz</td>
<td>57 ~ 63 Hz</td>
</tr>
</tbody>
</table>

2) Output

<table>
<thead>
<tr>
<th>ITEMS</th>
<th>−12V</th>
<th>+5.1V</th>
<th>+12V</th>
</tr>
</thead>
<tbody>
<tr>
<td>VOLTAGE REGULATION</td>
<td>−12V +2V/−1V</td>
<td>+/−3%</td>
<td>+/−3%</td>
</tr>
<tr>
<td>RIPPLE</td>
<td>—</td>
<td>50m Vp-p MAX</td>
<td>50m Vp-p MAX</td>
</tr>
<tr>
<td>OVER VOLTAGE</td>
<td>—</td>
<td>0 ~ 7V dc</td>
<td>—</td>
</tr>
</tbody>
</table>

* The output voltage is obtained by the Normal Load State.

(2) Circuit Description (Power Supply)

1) Input filter circuit

The AC input is filtered by C901, C904, L901 and is rectified by Bridge Rectifier (D901) and C905. The F901 is protected from overcurrent in the primary circuit. The C902, C903 is for restriction of leakage current and noise.

2) Driving circuit

The SNUBBER CIRCUIT consists of R903, C907, D902, C925 and is prevented from overshooting which is occurred between Ton and Toff of Q1 in the IC901. The Sensing Voltage is induced from the formula (N12=Nsenn=6/12*Ns+12) and is rectified in reverse, filtered by C906. Therefore, at pin 1 in the IC901, the sensing voltage is occurred to −6V and the Driving voltage is generated through Nb=N 1, 2 is flowed through R906, C911→C912→R905, C906, to the pin 2 of IC901. The C910, R904 is for preventing ringing and having soft start. The C909, C911 is for rising the speed of Driving Current. If the polarity of N12 is charged, the route of discharge is flowed through Transformer 2 →D904→C912→D903→Transformer 1. The D905 is for restricting the Driving Voltage.
2-3. Circuit Description

3) Output circuit
When transistor Q1 in the IC901 is OFF, the stored energy in the primary winding is delivered to the secondary circuit through the secondary winding.
The Output Voltage (+12V, +5V, −12V) is rectified by D907, D909, D912 and filtered by C913, C914, C916, C917, C923.
Therefore, by the regulator (IC902, IC903, IC905), the constant voltage is obtained.

4) Over voltage protection circuit (+5V)
The +5V is obtained by regulator (IC903) and then if OVER VOLTAGE is generated in the +5V line, by Bleeder resistor (R911, R912) and D911, the base of Q902 is biased.
Therefore, the current is flowed through Diode in IC904 and Q902 is ON.
The gate voltage is generated and the rectified voltage is flowed from pin 5 to pin 6 and Q901 is ON.
Therefore the base current of Q1 in the IC901 is decreased and the collector current is decreased.

(3) Circuit Description (CRT)

1) Video amplifier
Video amplification is provided by transistors Q301 and Q302. Transistor Q301 and Q302 are connected in a cascade configuration; Q301 operates as an common emitter and Q302 operates in the common base configuration.
This minimizes the Miller effect input capacitance and the defining breakdown parameter for Q302 becomes BvCBO as opposed to BvCEO for the common emitter configuration. This enables selection of a higher speed/lower breakdown transistor to be used in the video amplifier.
The video amplifier is normally off. That is, in the absence of a video input signal or with a signal level of less than 0.6V.
Then the CRT cathode voltage will be at +55V when the input signal exceeds 0.6V.
The amplifier can be adjusted by rotating subcontrast variable resistor VR 301.
Then the amplifier begins operation in the linear region and the CRT cathode voltage starts to decrease with a normal voltage gain of 35 V.
2-3. Circuit Description

2) Vertical deflection
The vertical electronics circuit consists of an IC701 (TDA 1170N) and the vertical deflection coil of the DY. (and associated circuitry)
The IC701 (TDA 1170N) incorporates all the functions for providing the yoke of the MONITOR with the current required for vertical deflection.
The preamplifier is a high input impedance differential type, with invented input available at PIN 10.
The non-inverting input is fixed internally in the circuit at a stabilized voltage of approximately 2.2 V.
During flyback, the flyback generator produces a voltage equal to approximately double the supply voltage and this is applied at the Yoke by means of the power amplifier.
The oscillator is a threshold type with a high degree of frequency stability.
It is a synchronized with a circuit which receives either positive or negative sync. pulses and ensures complete immunity from noise throughout most of the scanning time.
The time constant circuit that determines the vertical oscillation frequency consists of C602, R607 and VR601 connected at the PIN 9 of IC601.
The vertical size control function is performed by VR603 and R609 causing the negative feedback to change.

3) Horizontal deflection
The IC701 performs the horizontal synchronization (Oscillator) and Horizontal Drive.
The AFC circuit consists of the phase detection circuit of the IC701 and the associated component that connected to PIN 3 and PIN 4 of IC701.
The oscillation limit circuit is necessary to prevent the pulse from excessive high voltage.
This circuit is located in the IC701 and controls the oscillator to maintain the control signal in its correct frequency and in phase with the horizontal sync signal.
The oscillation frequency consists of C710 connected with the PIN 11 of IC701 and R714, VR702 connected with the PIN 12 of IC701.
Horizontal Drive pulse out of the IC709, PIN 8 are through T701 (Horizontal Drive Trans) to the base of Horizontal output Q703.
Transistor Q708 is based on when the beam is at about midscreen.
The charge stored on C724 causes current to flow through the Horizontal Yoke winding and Q703 to ground.
When the beam reaches the right side of the screen, Q703 is turned off, and the current in the Yoke is directed in C714.
At the same time, the current flows into C714 from the regulated B+ via to FBT primary winding.
Due to resonance, the current then reverses and flows back through the horizontal yoke winding into C724.
2-3. Circuit Description

4) Timing Chart

<table>
<thead>
<tr>
<th>Character</th>
<th>7x9 Dots</th>
</tr>
</thead>
<tbody>
<tr>
<td>Character Block</td>
<td>8x14 Dots</td>
</tr>
<tr>
<td>Character Clock</td>
<td>2.5 MHz</td>
</tr>
<tr>
<td>Dot Clock</td>
<td>20 MHz</td>
</tr>
</tbody>
</table>

### Horizontal Timing

**Video**

<table>
<thead>
<tr>
<th></th>
<th>TOTAL CHr</th>
<th>DISPLAY CHr</th>
<th>FRONT P</th>
<th>SYNC SIG</th>
<th>BACK p</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sync Rate 24.038 KHz</td>
<td>104</td>
<td>84</td>
<td>2</td>
<td>10</td>
<td>8</td>
</tr>
<tr>
<td>24.038 kHz</td>
<td>41.6 µS</td>
<td>33.6 µS</td>
<td>0.8 µS</td>
<td>4.0 µS</td>
<td>3.2 µS</td>
</tr>
</tbody>
</table>

### Vertical Timing

**Video**

<table>
<thead>
<tr>
<th></th>
<th>TOTAL CHr</th>
<th>DISPLAY CHr</th>
<th>FRONT P</th>
<th>SYNC SIG</th>
<th>BACK p</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sync Rate 60.096 Hz</td>
<td>400</td>
<td>344</td>
<td>14.0</td>
<td>16.0</td>
<td>26</td>
</tr>
<tr>
<td>60.096 Hz</td>
<td>16.640 mS</td>
<td>14.310 mS</td>
<td>0.582 mS</td>
<td>0.6656 mS</td>
<td>1.0816 mS</td>
</tr>
</tbody>
</table>
CHAPTER 3

REPAIR

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3-4. CPU PCB Unit ...................................... 3-35
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3. REPAIR

3-1. μFDD Unit
1. Disassembly - Reassembly

(1) Flowchart

![Flowchart Diagram]

Fig. 3-1
3-1. μFDD Unit

(2) Disassembly - reassembly

Fig. 3-2

Fig. 3-3
3-1. µFDD Unit
3-1. μFDD Unit

2. Electrical Adjustment

(1) Preparation

1) The check terminals are shown in Fig. 3-5.

2) We recommend to use an adapter as shown in Fig. 3-6, when making connections between measuring equipment and check terminals.

3) When making connections, ensure that the system is turned off before connecting any probes and turned on only after all connections are completed.

4) Confirm that any conductive materials are not stucked to the drive and then turn the system switch on.
3-1. $\mu$FDD Unit

(2) Index burst position adjustment

Adjustment purpose
This adjustment is made when the Index Sensor, etc. has been replaced. The sensor position is adjusted so that sector 1 is assigned from the correct position after index signal detection.

Adjustment procedure
1) Connect the Drive to the System.
2) The Probes of the Oscilloscope are connected as shown in Fig. 3-8 and the signal conditions are set up as shown in Fig. 3-7. The External Trigger must be: NORM-NEG.
3) Set the Alignment Disk in the Drive, and start FDD Adjustment. (See Chapter 4, Check Operation.)
4) Loosen the Mounting Screw on the Index Sensor and adjust its position so that the time between the sweep starting point and the burst starting point (indicated by “T” in Fig. 3-9) is within the adjusted range: 400 ± 400 $\mu$sec.
5) While taking care not to alter the precise adjustment, tighten the Mounting Screw on the Index Sensor.

Tools
Oscilloscope
Alignment Disk (TKC-0470)
Driver, Phillips type (+) (CK-0129)

<table>
<thead>
<tr>
<th>SWITCH</th>
<th>CH1</th>
<th>CH2</th>
</tr>
</thead>
<tbody>
<tr>
<td>AC-GND-DC</td>
<td>AC</td>
<td>AC</td>
</tr>
<tr>
<td>VERT</td>
<td>ADD</td>
<td></td>
</tr>
<tr>
<td>INVERT</td>
<td>—</td>
<td>ON</td>
</tr>
<tr>
<td>VOLT/DIV</td>
<td>0.1V</td>
<td>0.1V</td>
</tr>
<tr>
<td>TIME/DIV</td>
<td>0.1 msec.</td>
<td></td>
</tr>
<tr>
<td>CONNECTION TERMINAL</td>
<td>CHK1</td>
<td>CHK2</td>
</tr>
</tbody>
</table>

Fig. 3-7

Fig. 3-8

Fig. 3-9

Fig. 3-10
3-1. μFDD Unit

(3) Track position adjustment

Adjustment purpose
After the Stepping Motor, etc. has been replaced, the motor position is adjusted so that the Head is accurately stepped to the objective track.

Adjustment procedure
1) Connect the Drive to the System.
2) The Probes (1 : 1) of the Oscilloscope are connected as shown in Fig. 3-12 and the signal conditions are set up as shown in Fig. 3-11.
   The External Trigger must be: NORM, NEG.
3) Set the Alignment Disk in the Drive, and start FDD Adjustment. (See Chapter 4, Check Operation.)
4) Loosen the Mounting Screw on the Stepping Motor.
5) In Index Burst Position, turn the Eccentric Dowel for Adjustment so that the burst ratio of the wave forms (cats eye pattern) are within 85% to 100% of the burst ratio value obtained from burst ratio formula.

\[
\text{Burst ratio} = \frac{\text{Smaller reproduced voltage}}{\text{Larger reproduced voltage}} \times 100
\]

6) While taking care not to alter the precise head position setting, tighten the Mounting Screws on the Stepping Motor.

Confirmation
In step in and step out, check that the burst ratio is within 75% to 100% for both sides as to both case. If the burst ratio is outside of range, re-adjust.

Tools
Oscilloscope
Alignment Disk (TKC-0470)
Driver, Phillips type (−) (CK-0120)
Driver, Phillips type (+) (CK-0103)
3-1. µFDD Unit

(4) Track 00 position adjustment

Adjustment purpose
After the track 00 switch assembly, etc. has been replaced, its position is adjusted so that the head is accurately set to track 00.

Adjustment procedure
1) Connect the Drive to the System.
2) The Probes of the Oscilloscope are connected as shown in Fig. 3-16 and the signal conditions are set up as shown in Fig. 3-15. The External Trigger must be: INT, CH2, NEG.
3) Set the Normal Disk in the Drive, and start FDD Adjustment.
4) Select the Track 00 and move the head in alternate steps between track 00 and track 06.
5) Loosen the Mounting Screw on the track 00 switch assembly and adjust its position by turning the Eccentric Dowel so that the positive-going edge of waveforms displayed on the Oscilloscope will be within the range in Fig. 3-18. The starting point of the positive going edge (0.4V - 2.4V) shall be within the time range: 3.0 msec from the track 01 pulse.
6) While taking care not to alter the precise adjustment, tighten the Mounting Screw on the track 00 switch assembly.

Tools
Oscilloscope
Normal Disk
Driver, Phillips type (+) (CK-0103)
Driver, Phillips type (−) (CK-0120)
3-1. μFDD Unit

(5) Modulation

Adjustment purpose
After disassembly, reassembly, or adjustment, the reproduce output amplitude is checked as the final check.

Confirmation procedure
1) Connect the Drive to the System.
2) The Probes of the Oscilloscope are connected as shown in Fig. 3-20 and the signal conditions are set up as shown in Fig. 3-19. The External Trigger must be: NORM, NEG.
3) Set the Normal Disk in the Drive, and select the track 00 and the track 79 modulation, and observe the waveform.
4) Set the Normal Disk in the Drive B.
5) Confirm that the calculated values (ratios) using the following formula will be less than 10%.

\[
\text{Formula} = \frac{A - B}{A + B} \times 100
\]

A: Maximum value of output voltage
B: Minimum value of output voltage

Tools
Normal Disk
Oscilloscope

---

**Table 3-19**

<table>
<thead>
<tr>
<th>SWITCH</th>
<th>CH1</th>
<th>CH2</th>
</tr>
</thead>
<tbody>
<tr>
<td>AC-GND-DC</td>
<td>AC</td>
<td>AC</td>
</tr>
<tr>
<td>VERT</td>
<td>ADD</td>
<td>ON</td>
</tr>
<tr>
<td>INVERT</td>
<td></td>
<td>ON</td>
</tr>
<tr>
<td>VOLT/DIV</td>
<td>0.2V</td>
<td>0.2V</td>
</tr>
<tr>
<td>TIME/DIV</td>
<td>20 msec.</td>
<td></td>
</tr>
<tr>
<td>CONNECTION</td>
<td>CHK1</td>
<td>CHK2</td>
</tr>
<tr>
<td>TERMINAL</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

---

**Fig. 3-19**

**Fig. 3-20**

**Fig. 3-21**

---

**Diagram 3-19**

**Diagram 3-20**

**Diagram 3-21**
3-1. μFDD Unit
3. Circuit Diagram

(1) Defective block identification

① Rotation check

Set the normal disk in the drive, and start the check program. (Refer to Chapter 4. Check Operation)

Perform FDD Check

Sound Check

Is the DD motor rotating?

Yes

Is J2-Pin3 0.8V or below?

Yes

Replace the drive.

No

No

Is J2-Pin5 0.8V or below?

Yes

Is Motor on signal OK?

No

Replace the PCB Ass'y.

Check the I/O Cable.

No

Replace the drive.

END
3-1. µFDD Unit

② Index check

START

Rotation Check

Perform FDD Check

Is index signal OK?

No

Is J6-Pin1 0.8V or below?

No

Yes

Replace the Index Sensor.

Yes

Replace the PCB Ass'y.

END

③ Seek check

START

Start FDD Adjustment -Track 00-

Sound Check

Is seeking operation OK?

No

Step Pulse Check

Are the waveforms from J3-Pin 1, 2, 3 and 4 OK?

No

Is the Stepping Motor rotating?

No

Yes

Replace the Stepping Motor

Replace the PCB Ass'y.

Yes

Replace the Head Ass'y.

END
3-1. μFDD Unit

④ Track 00 check

START

Seek Check

Start FDD Adjustment
Track 00 of the
Check Program.

Track 00
signal OK?

Yes

No

Is CHK5
3.5V or
above?

No

Yes

Replace the PCB
Ass'y.

Replace the
Track 00 Switch
Ass'y.

DIRECTION IN
J1-12 (IN)

STEP
J1-13 (IN)

TRACK 00
J1-16 (OUT)

4.0 msec. (MAX)
3-1. μFDD Unit

5) Write check

START

Rotation Check

Index Check

Seek Check

Track 00 Check

Set the normal disk in the drive.

Start the FDD Check.

Is writing waveform OK?

Yes

Is a head wire broken?

No

Replace with a new Head Ass'y

Yes

Replace with a new PCB Ass'y.

No

END
3-1. μFDD Unit

Read check

START

Rotation Check

Index Check

Seek Check

Track 00 Check

Write Check

Is reading waveform OK? No

Replace the PCB Ass'y.

Is reading waveform OK? No

Replace with a new Head Ass'y.

Yes

Yes
3-2. CRT/Power Supply Unit (MATSUSHITA)

1. Disassembly - Reassembly

Warning: Before disassembly, discharge the High Voltage thoroughly with the procedures of (2)
Before repairing on page 3-18.

(1) Assembly location diagram
3-2. CRT/Power Supply Unit (MATSUSHITA)

Caution for servicing is indicated below.
Before removing the CRT, discharge the High Voltage thoroughly with the following procedures Step 1 to 3.

Warning: Great care must be taken because the High Voltage will be charged for approximately 30 hours or more after power is turned off. Discharge the High Voltage before assembling each of the units or servicing.

Procedures
Step 1. Remove the CRT Socket P.C.Board from the CRT.
Step 2. Discharge the High Voltage which has been charged in the CRT.
Step 3. Remove Anode Cap.

Discharge of High Voltage
### 3-2. CRT/Power Supply Unit (MATSUSHITA)

#### 2. Electrical Adjustment

<table>
<thead>
<tr>
<th>ADJUSTMENT ITEM</th>
<th>ADJUSTMENT POINT</th>
<th>TOOL</th>
<th>CHECK PROGRAM</th>
</tr>
</thead>
<tbody>
<tr>
<td>(1) Horizontal synchronization</td>
<td>R528</td>
<td>Driver</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(2) Vertical synchronization</td>
<td>R416</td>
<td>Driver</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(3) Vertical amplitude</td>
<td>R418</td>
<td>Driver</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(4) Vertical linearity</td>
<td>R417</td>
<td>Driver</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(5) Horizontal amplitude</td>
<td>L502</td>
<td>Ferrite Core Driver</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(6) Sub bright</td>
<td>R530</td>
<td>Driver</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(7) Focus</td>
<td>R529</td>
<td>Driver</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(8) Centering</td>
<td>Deflection coil centering magnet</td>
<td></td>
<td>H Pattern</td>
</tr>
<tr>
<td>(9) Deflection distortion</td>
<td>4-pole correction magnet</td>
<td>Plastic Square Driver</td>
<td>H Pattern</td>
</tr>
</tbody>
</table>

Note: Turn on the power and wait for the circuits to stabilize (30 minutes or more) before making any adjustments.
3-2. CRT/Power Supply Unit (MATSUSHITA)

(1) Horizontal synchronization
   (Adjustment purpose)
   Adjustment of the character area to the center of the raster.
   (Adjustment procedure)
   1) Turn R528 clockwise and find the point at which the screen is distorted to the right.
   2) Turn R528 counterclockwise and find the point at which the screen is distorted to the left.
   3) Set synchronization to the center of the left and right distortion of the screen.
   Note: The screen may not be distorted when R528 is turned clockwise or counterclockwise.
   In this case, assume that the MAX position of the VR is the point at which the screen is distorted.

(2) Vertical adjustment
   The adjustment purpose and procedure are the same as horizontal adjustment. The adjustment VR is R416.

(3) Vertical amplitude
   (Adjustment purpose)
   Adjustment of the vertical display length to 115 mm.
   (Adjustment procedure)
   1) Turn R418 clockwise or counterclockwise and adjust the vertical display length to 115 mm.
   Note: Vertical display length: 115 mm ± 2 mm.

(4) Vertical linearity
   (Adjustment purpose)
   Adjustment so that the height of the character H is the same at the top row and bottom row.
   (Adjustment procedure)
   1) Turn R417 clockwise and counterclockwise and adjust so that the length of the top and bottom characters is the same.

(5) Horizontal amplitude
   (Adjustment purpose)
   Adjustment of the horizontal display length to 170 mm.
   (Adjustment procedure)
   1) Turn L502 clockwise and counterclockwise and adjust the horizontal display length to 170 mm.
   Note: Horizontal display length: 170 mm ± 2 mm.
3.2. CRT/Power Supply Unit (MATSUSHITA)

(6) Brightness

(Adjustment purpose)
Adjustment to the point at which the back raster disappears at the BRIGHT VR MAX position.

(Adjustment procedure)
1) Set the BRIGHT VR (R531) to MAX.
2) Turn SUB BRIGHT R530 clockwise and counterclockwise and adjust it to the point at which the back raster disappears.

(7) Focus

(Adjustment purpose)
Adjustment to the position at which the entire screen is focused uniformly.

(Adjustment procedure)
1) Turn R529 clockwise and counterclockwise and adjust it to the position at which the center of the screen is focused uniformly.

(8) Centering
Adjust the deflection coil centering magnet so that the display area is at the center of the CRT.

(9) Deflection distortion
Correct the distortion by loosening the 4-pole correction magnet mounting screws and moving the magnet.
3. CIRCUIT DIAGRAM

USA/Canada

MAIN CIRCUIT

INPUT FILTER
RECTIFIER

EMI FILTER

VOLTAGE REGULATOR
CRT/Power Supply Unit (MATSUSHITA) Repairing Flowchart

3.2 CRT/Power Supply Unit (MATSUSHITA)
3.3. CRT/Power Supply Unit (GOLD STAR)

1. Disassembly - Reassembly

Warning: Before disassembly and discharge the High Voltage throughly with the procedures on page 3-18.
3-2. CRT/Power Supply Unit (GOLD STAR)

2. Electrical Adjustment

Main PCB

<table>
<thead>
<tr>
<th>ITEM</th>
<th>ADJUSTMENT POINT</th>
<th>TOOL</th>
<th>CHECK PROGRAM</th>
</tr>
</thead>
<tbody>
<tr>
<td>(1) Horizontal synchronization</td>
<td>VR 702</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(2) Vertical synchronization</td>
<td>VR 601</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(3) Vertical size</td>
<td>VR 603</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(4) Vertical linearity</td>
<td>VR 604</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(5) Horizontal size</td>
<td>L 703</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(6) Horizontal linearity</td>
<td>L 704</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(7) Brightness</td>
<td>100 KB (AT P703)</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(8) Focus</td>
<td>VR 704</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(9) Contrast</td>
<td>VR 301</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(10) Sub-bright</td>
<td>VR 703</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
</tr>
<tr>
<td>(11) Centering</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
<td></td>
</tr>
<tr>
<td>(12) Deflection distortion</td>
<td>Adjustment Tool Kit</td>
<td>H Pattern</td>
<td></td>
</tr>
</tbody>
</table>

Note: Turn on the power and wait for the circuits to stabilize (15 minutes or more) before making any adjustments.
3-3. CRT/Power Supply Unit (GOLD STAR)

Supplemental description
For items (1) to (6), refer to page 3-20.

(7) Brightness

(Adjustment purpose)
Adjustment to the point at which the back raster disappear at the BRIGHT VR MAX position.

(Adjustment procedure)
1) Set the BRIGHT 100KB (At P703) to max.
2) Turn SUB BRIGHT VR703 clockwise and counterclockwise and adjust it to the point at which the back raster disappears.

(8) FOCUS

(Adjustment purpose)
Adjustment to the position at which the entire screen is focused uniformly.

(Adjustment procedure)
Turn VR704 clockwise and counterclockwise and adjust it to the position at which the center of the screen is focused uniformly.

(9) Contrast

<table>
<thead>
<tr>
<th>Adjustment point</th>
<th>Method of Adjustment</th>
</tr>
</thead>
<tbody>
<tr>
<td>CONT (VR301)</td>
<td>Adjust to the contrast at which the characters are easy to see.</td>
</tr>
</tbody>
</table>
3-3. CRT/Power Supply Unit (GOLD STAR)

(11) Centering
Adjust the deflection coil centering magnet so that the display area is at the center of the CRT.

(12) Deflection distortion
① Correct the distortion by loosening the 4-pole correction magnet mounting screws and moving the magnet.

② Correct the distortion by turning the 4-pole correction magnet.
3-3. CRT/Power Supply Unit (GOLD STAR)

1) Trouble shooting chart

MAIN CHART

- Please check the monitor and the PCB. How to comment exactly.
  - Check signal pin 7, 8, 9.
    - Pin7=+5V? Pin8=+12V? Pin9=-12V?
      - No: Go to power chart.
      - Yes: Video something wrong?
        - Yes: Go to video chart.
        - No: Switch on monitor. Then check it after 15 minutes.
          - Something wrong?
            - No: Adjustment
              - Good! Please return the set to owner.
3-3. CRT/Power Supply Unit (GOLD STAR)

POWER CHART

- No power
  - Check signal cable
    - Pin7=5V? Yes
    - Pin8=12V? Yes
    - No or weak
      - Check 5 of T901
      - +140V DC? Yes
        - Check 3 of IC901
        - 300VDC-P? Yes
          - Trouble in IC901
        - No
          - Trouble in IC902 IC903 and IC905
    - No
      - Check fuse F902 OK?
      - No
        - Trouble in fuse ZA 250V
      - Yes
        - Go to the main chart
      - No
        - Check AC Fuse F901
          - Open? Yes
            - Trouble in D901
          - No Fuse re-open?
            - Yes Replace AC fuse
            - No
              - Replace line filter L901
  - Replace power switch

- Trouble in D907
  - IC902 input 3.2V?
    - Yes
      - Trouble in IC902
    - No
      - IC903 input 5.4V?
        - Yes
          - Trouble in D909
        - No
          - Trouble in IC903

- Trouble in D912
  - IC905 input 3.0V?
    - Yes
      - Trouble in IC905
    - No
      - Trouble in IC905
3-3. CRT/Power Supply Unit (GOLD STAR)

VIDEO CHART

Adjust
V-Hold(VR601)
H-Hold(VR702)
Sub-contrast(VR301)

OK?

Check video chart.
Check horizontal circuit
Check vertical circuit

To the main chart.

Check the connection between monitor and PCB.

Display?

Yes

No

Check 1 of input cable.

3.5VP-P?

Yes

No

Trouble in signal cable or logic board.

Check base of Q301.

Z.OVP-P?

Yes

No

Trouble in VR301 adjustment or junction wire V1.

Check cathode of CRT PCB

ZBWP-P?

Yes

No

Check Q301, Q302 and V2 voltage(+50V).

Check heater voltage (+12V)
and Q2 voltage (+400V)
and Q1 voltage (+15,-50V)

Normal?

Yes

No

Trouble in Q301
Q302, D705, R730, C721.

Normal?

Yes

No

Trouble in CRT.
3. CRT/Power Supply Unit (GOLD STAR)
3-4. CPU PCB Unit

1. Circuit Diagram

To repair PC boards, use the following method:

1. Check if the DT MF sending level is normal. (If not, check the carrier circuit.)
2. Check if the CRT receiver circuit is normal. (If not, check the CRT receiver circuit.)
3. Check if the CRT driver circuit is normal. (If not, check the CRT driver circuit.)
4. Check if the CRT display is normal. (If not, check the CRT display.)
5. Check if the CRT circuit is normal. (If not, check the CRT circuit.)
6. Check if the CRT circuit is normal. (If not, check the CRT circuit.)
7. Check if the CRT circuit is normal. (If not, check the CRT circuit.)
8. Check if the CRT circuit is normal. (If not, check the CRT circuit.)
9. Check if the CRT circuit is normal. (If not, check the CRT circuit.)
10. Check if the CRT circuit is normal. (If not, check the CRT circuit.)

3-35
3.4. CPU PCB Unit

The waveforms after the completion of initialization

The waveform can only be seen when the power switch is turned on.

The waveforms are generated after self check is started 15 seconds.
Connect the telephone and the line to the jacks respectively. and observe the following waveforms.

- Ring tone signal at answering
- Waveforms do not appear during the standby period.
  The waveforms can only be seen during DTMF operation.
- These waveforms are generated when the DTMF tone is used for Auto Dial.
- Waveforms when the DTMF tone is used for automatically dial "2".
- Waveforms when the carrier is received 1200 bps: PSK
  The waveforms are not included during the standby period.
  Waveforms are used only for DTMF. The waveforms do not appear during the standby period.
  Waveforms when the DTMF tone is used for Auto Dial.
  Waveforms when the DTMF tone is used for Auto Dial.
  Waveforms when the DTMF tone is used for Auto Dial.
  Waveforms when the DTMF tone is used for Auto Dial.

Dial Pulse "3" at transmission to Auto Dial.
1. Keyboard Unit

2-5. Keyboard Unit

---

Table:

<table>
<thead>
<tr>
<th>Position</th>
<th>Symbol</th>
<th>Mark</th>
<th>Destination</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 0 0</td>
<td></td>
<td></td>
<td>U.S.A.</td>
</tr>
<tr>
<td>1 0 0</td>
<td></td>
<td></td>
<td>Canada</td>
</tr>
<tr>
<td>0 1 0</td>
<td></td>
<td></td>
<td>Asia</td>
</tr>
<tr>
<td>1 1 0</td>
<td></td>
<td></td>
<td>Oceania</td>
</tr>
</tbody>
</table>

---

Legend:

- DIP SW
- COC
- Symbol
- Mark
- Position

---

Notes:

- Set the country code under the DIP switch.
- For countries other than U.S., Canada, Asia, Oceania, set the country code under the DIP switch.

---

Diagram:

- Key board connector diagram.
- Various symbols and connections.

---

3-5: Keyboard Unit

---

4-4: Keyboard Unit
3-5. Keyboard Unit

2. Keyboard Circuit (Quebec)
3-5. Keyboard Unit

3. Keyboard Circuit (Latin)
3-5. Keyboard Unit

4. Keyboard Circuit (Norway)
3-5. Keyboard Unit

5. Keyboard Circuit (Denmark)
3-5. Keyboard Unit

6. Keyboard Circuit (Sweden/Finland)
3-5. Keyboard Unit

7. Keyboard Circuit (Netherlands)
3-5. Keyboard Unit

8. Keyboard Circuit (W. Germany)
3.5. Keyboard Unit

9. Keyboard Circuit (Switzerland)
### 3-5. Keyboard Unit

#### 10. Keyboard Circuit (France)

![Keyboard Circuit Diagram](image-url)
3-5. Keyboard Unit

11. Keyboard Circuit (U.K.)
3-5. Keyboard Unit

12. Keyboard Circuit (Spain)
3-5. Keyboard Unit

13. Keyboard Circuit (Italy)
3-5. Keyboard Unit

14. Keyboard Circuit (S. Africa)
3-5. Keyboard Unit

15. Keyboard Circuit (Japan)
CHAPTER 4

CHECK OPERATION

Page  
4-1. Program Name ................... 4-1  
4-2. Check Range/Specification ....... 4-2  
4-3. Operation ...................... 4-4  
4-4. Check Points for OK Results ...... 4-9  
4-5. Concrete Examples of NG Results ... 4-12  
4-6. ROM Check Sum Table ............ 4-20
4. CHECK OPERATION

Check programs are provided in the main unit system ROM. These programs are roughly divided into programs that make checks automatically (called self diagnosis) and programs that make checks by selecting the check items (called manual check). The manual check menu is displayed after self diagnosis is executed. The check items of each program are shown below.

4-1. Program Name

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Item</th>
<th>Required Item</th>
<th>Required Time</th>
<th>Field/Work Shop</th>
</tr>
</thead>
<tbody>
<tr>
<td>SELF DIAGNOSIS</td>
<td>① KB Country Code</td>
<td>None</td>
<td>20 sec.</td>
<td>Field/Work Shop</td>
</tr>
<tr>
<td></td>
<td>② SYS ROM</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>③ SV-ROM</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>④ SYS RAM</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>⑤ SV-RAM</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MANUAL CHECK</td>
<td>[1] Keyboard Check</td>
<td>None</td>
<td>—</td>
<td></td>
</tr>
<tr>
<td></td>
<td>[2] CRT Adjustment</td>
<td>None</td>
<td>—</td>
<td></td>
</tr>
<tr>
<td></td>
<td>[3] FDD Adjustment</td>
<td>Alignment Disk</td>
<td>—</td>
<td>Work Shop</td>
</tr>
<tr>
<td></td>
<td>[1] Index Burst Position</td>
<td>Alignment Disk</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>[2] Step In</td>
<td>Alignment Disk</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>[3] Step Out</td>
<td>Normal Disk</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>[4] Track 00</td>
<td>Normal Disk</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>[5] Track 00 Modulation</td>
<td>Normal Disk</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>[6] FDD Check</td>
<td>Normal Disk</td>
<td>—</td>
<td></td>
</tr>
</tbody>
</table>

Note:
① ~ ⑤: Checked automatically in this order.
[1] ~ [6]: Menu selection items
### 4-2. Check Range/Specification

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Check Range</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>SELF DIAGNOSIS</td>
<td>KB Country Code</td>
<td>( \text{Reads the country code (including the test SW status) from the keyboard, then displays the code and the country name. Country Code: D4 ~ D0 are country codes. D7 is test SW status.} )</td>
</tr>
<tr>
<td></td>
<td>CPU PCB Unit</td>
<td></td>
</tr>
<tr>
<td></td>
<td>KB Country Code</td>
<td>( \text{Reads the country code (including the test SW status) from the keyboard, then displays the code and the country name. Country Code: D4 ~ D0 are country codes. D7 is test SW status.} )</td>
</tr>
<tr>
<td></td>
<td>CPU PCB Unit</td>
<td></td>
</tr>
<tr>
<td>2 SYS ROM</td>
<td>CPU PCB Unit</td>
<td>( \text{Checks the system ROM check sum.} )</td>
</tr>
<tr>
<td></td>
<td>CPU PCB Unit</td>
<td></td>
</tr>
<tr>
<td>3 SV-ROM</td>
<td>CPU PCB Unit</td>
<td>( \text{Checks the SV-ROM check sum.} )</td>
</tr>
<tr>
<td></td>
<td>CPU PCB Unit</td>
<td></td>
</tr>
</tbody>
</table>
| 4 SYS RAM               | CPU PCB Unit         | \( \text{Performs system RAM read/write check. After checking, RAM No. is displayed. (00 ~ 33)} \)
|                         | CPU PCB Unit         | \( \text{If there is an error, the RAM No. is highlighted.} \) |
|                         | CPU PCB Unit         |               |
| 5 SV-RAM                | CPU PCB Unit         | \( \text{Performs SV-RAM size check.} \) |
|                         | CPU PCB Unit         |               |
| MANUAL CHECK            | Keyboard Check       | \( \text{Displays the keyboard layout and country name, then you manually selects check items using the cursor. If an error occurs, the check item is highlighted and the cursor moves to the next check item. Returns to the check menu, if a selection is made after the last check item is checked.} \) |
|                         | Keyboard Unit        |               |
|                         | CRT/Power Supply Unit| \( \text{Five patterns are available for CRT/POWER SUPPLY unit adjustment. Adjustments are made using the H-pattern. (The other patterns are for factory use.)} \) |
| 1 Keyboard Check        | Keyboard Unit        |               |
|                         | CRT/Power Supply Unit|               |
| 2 CRT Adjustment        | CRT/Power Supply Unit|               |
### 4-2. Check Range/Specification

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Check Range</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>MANUAL CHECK</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>[3] FDD Adjustment</td>
<td>μFDD Unit</td>
<td>[3] μFDD adjustment use&lt;br&gt;Six selections are possible. Adjustments are made by combining these selections.&lt;br&gt;[1] Index Burst Position&lt;br&gt;Reads the disk by stepping from track 00 to track 40.&lt;br&gt;[2] Step In&lt;br&gt;Items to be input while Index Burst Position is executing. Seeks to track 39 and steps to track 40.&lt;br&gt;[3] Step Out&lt;br&gt;Items to be selected after Step In was performed. Seeks to track 41 and steps to track 40.&lt;br&gt;[4] Track 00&lt;br&gt;Steps alternately between track 00 and track 06.&lt;br&gt;[5] Track 00 Modulation&lt;brWrites and reads Data = $55 at track 00.&lt;br&gt;[6] Track 79 Modulation&lt;br&gt;Steps from track 00 to track 79 and writes and reads Data = $55.</td>
</tr>
<tr>
<td>[4] FDD Head Cleaning</td>
<td>CPU PCB Unit</td>
<td>Cleans the μFDD heads. SELECTs tracks at random and loads the heads for five seconds. After executing, returns to check menu.</td>
</tr>
<tr>
<td>[5] Format Disk</td>
<td>μFDD Unit</td>
<td>Format a unformat disk to be a Read/Write disk.&lt;br&gt;This format is the same as the CAT's, but ID No. for the CAT cannot be written on the check disk.</td>
</tr>
<tr>
<td>[6] FDD Check</td>
<td>μFDD Unit</td>
<td>Performs FD read/write check.&lt;br&gt;The following methods are available:&lt;br&gt;1. Step In (Read/write from track 00 to track 79)&lt;br&gt;2. Step In/Out (Outside edge and inside edge read/write alternately)&lt;br&gt;3. Fixed (Read/write fixed address)</td>
</tr>
</tbody>
</table>
4-3. Operation

1. Starting Procedure

*SW2: SW2 is on the rear panel of the main unit. It is turned on by inserting the tip of a pen into the small hole to the left of the CENTRO connector.
4-3. Operation

2. Operation Method

(1) Manual Check

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Operation Method</th>
<th>Interruption/ Cancellation</th>
</tr>
</thead>
</table>
| Keyboard Check    | 1) When key 1 is pressed at the selection menu, the keyboard layout and country name are displayed.  
2) First, the leftmost key of the top row is highlighted.  
3) When a key is pressed, the next key is highlighted. Then, the highlighting moves from left to right and top to bottom.  
4) Detection of a wrong key generates a beep and the selection to the left remains highlighted.  
5) If a key is input after the last selection is tested, the display returns to the check menu.                                                                                                                                  | None                        |
| CRT Adjustment    | 1) When key 2 is pressed at the selection menu, H pattern is displayed.  
2) Five patterns are available. The H pattern is the first pattern.  
3) The pattern changes each time the SPACE key is pressed. (1, 2, 3, 4, 5, 1, 2, 3, ...)  
4) The current pattern is reversed each time the UNDO key is pressed. (Black on white to white on black)  
5) When the RETURN key is pressed, the display returns to the check menu.                                                                                                           | Cancellation RETURN         |
| FDD Adjustment    | When key 3 is pressed at the selection menu, FDD Adjustment is displayed. The following are displayed:  
[1] Index Burst Position  
[2] Step In  
[3] Step Out  
[4] Track 00  
[5] Track 00 Modulation  
### 4-3. Operation

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Operation Method</th>
<th>Interruption/ Cancellation</th>
</tr>
</thead>
<tbody>
<tr>
<td>FDD Adjustment</td>
<td>An operation is performed by combining several items from items [1] to [6] according to the purpose for FDD adjustment.</td>
<td>Cancellation RETURN</td>
</tr>
<tr>
<td></td>
<td>Operation is described below by purpose.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>To exit from this menu to the check menu, press the RETURN key.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>A. Track Position Adjustment</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1) Insert an alignment disk into the drive.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2) Press key [1] at the adjustment menu. (Index Burst Position selected.)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>(If there is no disk in the drive, a beep is generated and the selection is not performed.)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>3) Observe and adjust the waveform.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>4) Press key [2] and check the waveform.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>5) Press key [3] and check the waveform. Then, press the UNDO key (Quit)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>To interrupt the program at steps 3) and 4), press the UNDO key.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>B. Index Burst Position Adjustment</td>
<td>Interruption UNDO</td>
</tr>
<tr>
<td></td>
<td>1) Insert an alignment disk into the drive.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2) Press key [1] at the adjustment menu. (If there is no disk in the drive, a beep is generated and the selection is not performed.)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>3) Observe and adjust the waveform. Then, press the UNDO key (Quit)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>C. Track 00 Adjustment</td>
<td>Interruption UNDO</td>
</tr>
<tr>
<td></td>
<td>1) Insert a normal disk into the drive.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2) Press key [4] at the adjustment menu. (If there is no disk in the drive, a beep is generated and the selection is not performed.)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>3) Observe and adjust the waveform. Then, press the UNDO key (Quit)</td>
<td></td>
</tr>
</tbody>
</table>
### 4-3. Operation

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Operation Method</th>
<th>Interruption/ Cancellation</th>
</tr>
</thead>
<tbody>
<tr>
<td>FDD Adjustment</td>
<td>D. Modulation</td>
<td>Interruption UNDO</td>
</tr>
<tr>
<td></td>
<td>1) Insert a normal disk into the drive.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2) Press key [5] at the adjustment menu. (If there is no disk in the drive, a beep is generated and the selection is not performed.)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>3) Check the waveform. Then, press UNDO key.</td>
<td></td>
</tr>
<tr>
<td>FDD Check</td>
<td>1) When key [6] is pressed at the manual check menu, the FDD check screen is displayed.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>FDD Check:</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Sequence = Step In</td>
<td>Retry Count = 0</td>
</tr>
<tr>
<td></td>
<td>2) Each time the [LEAP] key is pressed, the cursor moves to the item at which a parameter is to be input. (The parameter at the cursor position is displayed at the bottom of the screen.)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>3) The parameters that can be input are:</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Item</td>
<td>Parameter</td>
</tr>
<tr>
<td></td>
<td>Retry Count =</td>
<td>[0 - 9]</td>
</tr>
<tr>
<td></td>
<td>Recalibrate Count =</td>
<td>[0 - 9]</td>
</tr>
<tr>
<td></td>
<td>Wr/Rd Mode =</td>
<td>[R] = Read only, [W] = Write Before Read Check</td>
</tr>
<tr>
<td></td>
<td>Track =</td>
<td>[0 - 79]</td>
</tr>
<tr>
<td></td>
<td>Sector =</td>
<td>[0 - 9]</td>
</tr>
<tr>
<td></td>
<td>The characters in [ ] are input.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>4) After setting all the parameters, insert a normal disk into the drive. (Write protect tab must be removed)</td>
<td></td>
</tr>
</tbody>
</table>

4-7
### 4-3. Operation

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Operation Method</th>
<th>Interruption/Cancellation</th>
</tr>
</thead>
<tbody>
<tr>
<td>FDD Check</td>
<td>5) Start the program by pressing [SPACE] key.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>6) “Pass:” is displayed. This shows the number of times FDD was checked. (Loop Count)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>7) To interrupt the program, press [UNDO] key for about one second. The display returns to the check screen.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>8) To exit from the FDD Check program, press the [RETURN] key.</td>
<td>Interruption [UNDO]</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Cancellation [RETURN]</td>
</tr>
<tr>
<td>FDD Head Cleaning</td>
<td>1) When the [4] is input at the check screen, the following message is displayed: Insert Cleaning Disk and press [SPACE] when ready. Press [RETURN] to return to main menu.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2) Insert a cleaning disk into the drive.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>3) Press the [SPACE] key.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>4) Five second later, the display returns to the check screen.</td>
<td></td>
</tr>
<tr>
<td>Format</td>
<td>Format: Before executing the FDD Check, the disk to be used must be initialized the following program:</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1) Remove the write-protect tab of a normal disk and insert the disk into the drive.</td>
<td></td>
</tr>
</tbody>
</table>
### 4-4. Check Points for OK Results

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Check Point</th>
</tr>
</thead>
<tbody>
<tr>
<td>SELF DIAGNOSIS</td>
<td>At the end of the checks, the results are displayed as follows:</td>
</tr>
</tbody>
</table>

**Self Diagnosis**

1. KB Country Code: 80  
   United States

2. ROM Checksums:  
   True Read  
   SYS ROM0 HIGH: xxxxxxxx xxxxxxxx  
   SYS ROM0 LOW: xxxxxxxx xxxxxxxx  
   SYS ROM1 HIGH: xxxxxxxx xxxxxxxx  
   SYS ROM1 LOW: xxxxxxxx xxxxxxxx  
   SYV ROM0: xxxxxxxx xxxxxxxx  
   SYV ROM1: xxxxxxxx xxxxxxxx  
   SYV ROM2: xxxxxxxx xxxxxxxx

3. SYSTEM RAMs:  
   00 01 02 03 10 11 12 13 20 21 22 23 30 31 32 33  
   SV RAM: 00002000

   [1] Keyboard Check  
   [2] CRT Adjustment  
   [3] FDD Adjustment  
   [4] FDD Head Cleaning  
   [6] FDD Check

1. The keyboard country code and country name are displayed.  
The names and codes are shown below. When D7 = High, “Country Code = 80” is displayed.  
00: United States  01: Canada  02: United Kingdom  03: Norway  
04: France  05: Denmark  06: Sweden  07: Japan  
08: West Germany  09: Netherlands  0A: Spain  0B: Italy  
0C: Latin America  0D: South Africa  0E: Switzerland

2. The system ROM and SV-ROM check sum is displayed. The sum value is displayed as true value (true) and check result value (read). Whether these two values are the same is checked.
### 4-4. Check Points for OK Results

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Check Point</th>
</tr>
</thead>
<tbody>
<tr>
<td>SELF DIAGNOSIS</td>
<td>③ The RAM1 to RAM15 and IC No. correspondence and installation state and size are shown below. RAM size must be determined using the table below.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>IC No.</th>
<th>RAM No.</th>
<th>Installation state</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>IC29 ~ IC26</td>
<td>RAM00 ~ RAM03</td>
<td>Standard</td>
<td>128 KB</td>
</tr>
<tr>
<td>IC25 ~ IC22</td>
<td>RAM10 ~ RAM13</td>
<td>Standard</td>
<td>128 KB</td>
</tr>
<tr>
<td>IC21 ~ IC18</td>
<td>RAM20 ~ RAM23</td>
<td>Optional RAM</td>
<td>128 KB</td>
</tr>
<tr>
<td>IC17 ~ IC14</td>
<td>RAM30 ~ RAM33</td>
<td>Uninstalled RAM</td>
<td>———</td>
</tr>
</tbody>
</table>

④ SV-RAM checks RAM size. “00002000” is displayed (8 kbytes).

<table>
<thead>
<tr>
<th>MANUAL CHECK</th>
<th>[1] Keyboard check</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>If all checks are OK, a beep is not generated and character highlighting does not occur.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>[2] CRT adjustment</th>
<th>The adjustment pattern does not use OK/NG messages.</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. H-pattern</td>
<td>(Field/work shop)</td>
</tr>
<tr>
<td>2. #H-pattern</td>
<td>(Factory)</td>
</tr>
<tr>
<td>3. Cross-pattern</td>
<td>(Factory)</td>
</tr>
<tr>
<td>4. Character-pattern</td>
<td>(Factory)</td>
</tr>
<tr>
<td>5. All dot-pattern</td>
<td>(Factory)</td>
</tr>
</tbody>
</table>
4-4. Check Points for OK Results

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Reading the result</th>
</tr>
</thead>
</table>
| MANUAL CHECK | [3] FDD adjustment  
The adjustment program does not use OK/NG messages. |
|              | [6] FDD Check       
If all checks are OK, a message is not displayed.  
Note) When the disk is write-protected, checks are not performed.  
When an error occurs, a beep is generated and the following message is displayed:  
Pass: 1  Track: 27  Sector: 6  Error: 6  Type: Write Error  
|              | Error Message       
|              | Error code          
|              | Sector No.          
|              | Track No.           
|              | Loop Count          |
|              | Type “Write Error” or “Read Error” |
|              | Error code:         |
|              | Error Code          |
|              | 1 No disk in drive  |
|              | 2 Seek error        |
|              | 3 Disk write protected |
|              | 4 No headers found  |
|              | 5 Header CRC error  |
|              | 6 No data field found |
|              | 7 Data CRC error    |
|              | 8 Disk verify error |
|              | 9 Wrong header found |
|              | [4] FDD Head Cleaning  
Since this is a head-cleaning program, OK/NG messages are not displayed. |
4-5. Concrete Examples of NG Results

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Concrete Examples of NG Results</th>
</tr>
</thead>
<tbody>
<tr>
<td>SELF DIAGNOSIS</td>
<td></td>
</tr>
<tr>
<td>① KB Country Code</td>
<td>Country code is different.</td>
</tr>
<tr>
<td>② SYS ROM and</td>
<td>The true value is different from the read value.</td>
</tr>
<tr>
<td>SV-ROM</td>
<td></td>
</tr>
<tr>
<td>③ RAM and SV-RAM</td>
<td>“Error” is displayed.</td>
</tr>
<tr>
<td></td>
<td>RAM size is different.</td>
</tr>
<tr>
<td>MANUAL CHECK</td>
<td></td>
</tr>
<tr>
<td>[1] Keyboard</td>
<td>Keys on the key layout are highlighted. (Some or all of the keys)</td>
</tr>
<tr>
<td></td>
<td>Even if a key is pressed, it is not input.</td>
</tr>
</tbody>
</table>
### 4-5. Concrete Examples of NG Results

<table>
<thead>
<tr>
<th>Cause of Fault</th>
<th>Action</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>Defective Diode (CC-Line)</td>
<td>Replace the keyboard unit.</td>
<td>Keyboard Check</td>
</tr>
<tr>
<td>Poor Cable</td>
<td></td>
<td></td>
</tr>
<tr>
<td>System ROM or SV-ROM, defective</td>
<td>Replace the CPU PCB unit.</td>
<td>Replace the ROM chip or observe the waveform.</td>
</tr>
<tr>
<td>RAM Chip, defective</td>
<td>Replace the CPU PCB unit.</td>
<td>Replace the RAM chip and Gate Array #1 or #2.</td>
</tr>
<tr>
<td>Gate Array #1 or #2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>RAM chip, defective</td>
<td>Replace the CPU PCB unit.</td>
<td>Replace the RAM chip.</td>
</tr>
<tr>
<td>Key switch, defective</td>
<td>Replace the keyboard unit.</td>
<td>Keyboard check</td>
</tr>
<tr>
<td>Diode, defective</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Poor Cable, Gate Array #3, defective</td>
<td>Replace the keyboard unit</td>
<td>Cable check</td>
</tr>
<tr>
<td></td>
<td>Replace the CPU PCB unit.</td>
<td>Replace the Gate Array #3.</td>
</tr>
</tbody>
</table>
### 4-5. Concrete Examples of NG Results

<table>
<thead>
<tr>
<th>Program Name</th>
<th>NG symptoms</th>
<th>OK Examples</th>
</tr>
</thead>
<tbody>
<tr>
<td>MANUAL CHECK</td>
<td>The CRT display is vertically long or short. The lengths of the characters at the top and bottom of the CRT are different.</td>
<td>H Patterns of uniform length are displayed at the top and bottom.</td>
</tr>
<tr>
<td>[2] CRT Adjustment</td>
<td><img src="image1.png" alt="NG Example" /></td>
<td><img src="image2.png" alt="OK Example" /></td>
</tr>
<tr>
<td></td>
<td>The CRT display is slanted.</td>
<td>H Patterns of uniform length are displayed at the top, bottom, left, and right.</td>
</tr>
<tr>
<td></td>
<td><img src="image3.png" alt="NG Example" /></td>
<td><img src="image4.png" alt="OK Example" /></td>
</tr>
<tr>
<td></td>
<td>The CRT display is distorted.</td>
<td>H Patterns of uniform length are displayed at the top, bottom, left, and right.</td>
</tr>
<tr>
<td></td>
<td><img src="image5.png" alt="NG Example" /></td>
<td><img src="image6.png" alt="OK Example" /></td>
</tr>
<tr>
<td></td>
<td>The CRT display drifts horizontally or vertically.</td>
<td>H Patterns of length are displayed uniformly at the top, bottom, left, and right.</td>
</tr>
<tr>
<td></td>
<td><img src="image7.png" alt="NG Example" /></td>
<td><img src="image8.png" alt="OK Example" /></td>
</tr>
</tbody>
</table>
## 4-5. Concrete Examples of NG Results

<table>
<thead>
<tr>
<th>Cause of Fault</th>
<th>Action</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>Vertical amplitude maladjustment</td>
<td>Replace the CRT/Power supply unit.</td>
<td>Adjust the vertical amplitude VR.</td>
</tr>
<tr>
<td>Vertical linearity maladjustment</td>
<td></td>
<td>MATSUSHITA: R418</td>
</tr>
<tr>
<td></td>
<td></td>
<td>GOLD STAR: VR603</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Vertical linearity maladjustment</td>
</tr>
<tr>
<td></td>
<td></td>
<td>MATSUSHITA: R417</td>
</tr>
<tr>
<td></td>
<td></td>
<td>GOLD STAR: VR604</td>
</tr>
<tr>
<td>CRT internal deflection coil</td>
<td>Replace the CRT/Power supply unit.</td>
<td>Adjust the deflection coil.</td>
</tr>
<tr>
<td>maladjustment</td>
<td></td>
<td></td>
</tr>
<tr>
<td>CRT internal centering magnet</td>
<td>Replace the CRT/Power supply unit</td>
<td>Adjust the centering magnet.</td>
</tr>
<tr>
<td>maladjustment</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Horizontal synchronization maladjustment</td>
<td>Replace the CRT unit.</td>
<td>Adjust the horizontal/vertical synchronization VR.</td>
</tr>
<tr>
<td>Vertical synchronization maladjustment</td>
<td></td>
<td>MATSUSHITA: R528/R416</td>
</tr>
<tr>
<td></td>
<td></td>
<td>GOLD STAR: VR702/VR601</td>
</tr>
<tr>
<td>Defective HSYNC, VSYNC signal</td>
<td>Replace the CPU PCB unit.</td>
<td>Check the monitor signal.</td>
</tr>
</tbody>
</table>
### 4-5. Concrete Examples of NG Results

<table>
<thead>
<tr>
<th>Program Name</th>
<th>NG symptoms Display/Printing</th>
<th>OK Examples Display/Printing</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>MANUAL CHECK</strong></td>
<td>No display on the CRT.</td>
<td>H Patterns are displayed.</td>
</tr>
<tr>
<td>[2] CRT Adjustment</td>
<td>The CRT display is too bright or too dark.</td>
<td>The CRT display is not definite.</td>
</tr>
<tr>
<td></td>
<td>H Patterns can be clearly seen.</td>
<td>H Patterns are displayed.</td>
</tr>
<tr>
<td></td>
<td>The CRT displays only one vertical line.</td>
<td>The CRT displays only one horizontal line.</td>
</tr>
<tr>
<td></td>
<td>[CRT monitor] [CRT monitor]</td>
<td>[CRT monitor] [CRT monitor]</td>
</tr>
<tr>
<td></td>
<td>The CRT display is misaligned vertically.</td>
<td>The CRT display is misaligned horizontally.</td>
</tr>
<tr>
<td></td>
<td>H Patterns are displayed in the center of the CRT at the top and bottom, and at the left and right.</td>
<td>H Patterns are displayed in the center of the CRT at the top and bottom, and at the left and right.</td>
</tr>
</tbody>
</table>
4-5. Concrete Examples of NG Results

<table>
<thead>
<tr>
<th>Cause of Fault</th>
<th>Action</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>Defective CRT internal circuit</td>
<td>Replace the CRT/Power supply unit.</td>
<td>Check the video circuit.</td>
</tr>
<tr>
<td>Defective CRT cable</td>
<td>Replace the CRT/Power supply unit.</td>
<td>Check the CRT Cable.</td>
</tr>
<tr>
<td>Heater is disconnected.</td>
<td>Replace the CRT/Power supply unit.</td>
<td>Check the monitor signal.</td>
</tr>
<tr>
<td>Defective CRTC/VRAM circuit.</td>
<td>Replace the CRT/Power supply unit.</td>
<td>Check the CRT/VRAM circuit.</td>
</tr>
<tr>
<td>CRT external intensity VR maladjustment.</td>
<td>Adjust the VR.</td>
<td>Adjust the intensity VR.</td>
</tr>
<tr>
<td>CRT internal intensity VR maladjustment.</td>
<td>Replace the CRT/Power supply unit.</td>
<td>MATSUSHITA: R530</td>
</tr>
<tr>
<td>Defective focusing VR</td>
<td>Replace the CRT/Power supply unit.</td>
<td>Adjust the focus VR.</td>
</tr>
<tr>
<td>Contrast maladjustment.</td>
<td></td>
<td>GOLD STAR: VR703</td>
</tr>
<tr>
<td></td>
<td></td>
<td>MATSUSHITA: R530</td>
</tr>
<tr>
<td></td>
<td></td>
<td>GOLD STAR: VR703</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Adjust the contrast VR.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>MATSUSHITA: R530</td>
</tr>
<tr>
<td></td>
<td></td>
<td>GOLD STAR: VR301</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Defective CRT internal circuit.</td>
<td>Replace the CRT/Power supply unit.</td>
<td>Check the CRT internal circuit.</td>
</tr>
<tr>
<td>Defective video signal</td>
<td>Replace the CPU PCB unit.</td>
<td>Check the monitor circuit.</td>
</tr>
<tr>
<td>CRT internal centering magnet</td>
<td>Replace the CRT/Power supply unit.</td>
<td>Adjust the CRT internal centering magnet.</td>
</tr>
<tr>
<td>maladjustment</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
### 4-5. Concrete Examples of NG Results

<table>
<thead>
<tr>
<th>Program Name</th>
<th>NG symptoms</th>
<th>OK Examples</th>
</tr>
</thead>
<tbody>
<tr>
<td>MANUAL CHECK [2]</td>
<td>The CRT display is horizontally expanded, or the widths of the characters</td>
<td>Pattern Hs are displayed in the center of the CRT. The widths of the</td>
</tr>
<tr>
<td>CRT Adjustment</td>
<td>on the left and right sides of a narrowed display are different.</td>
<td>displayed characters are the same at the top and bottom, and at the left</td>
</tr>
<tr>
<td></td>
<td></td>
<td>and right.</td>
</tr>
</tbody>
</table>

#### Display/Printing

![Image of CRT Display](image)

#### Concrete Examples of NG Results

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Concrete Examples of NG Results</th>
</tr>
</thead>
<tbody>
<tr>
<td>MANUAL CHECK [6]</td>
<td>FDD Check</td>
</tr>
<tr>
<td></td>
<td>Read Error</td>
</tr>
<tr>
<td></td>
<td>Write Error</td>
</tr>
<tr>
<td></td>
<td>The FDD is not starting.</td>
</tr>
</tbody>
</table>

![Image of FDD Result](image)
### 4-5. Concrete Examples of NG Results

<table>
<thead>
<tr>
<th>Cause of Fault</th>
<th>Action</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>Horizontal amplitude maladjustment</td>
<td>Replace the CRT/Power supply unit.</td>
<td>Adjust the horizontal amplitude VR. MATSUSHITA: L502 GOLDSTAR:</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Cause of Fault</th>
<th>Action</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>FDD or Disk CPU PCB (Gate Array #3)</td>
<td>Replace the FDD unit or disk. Replace the CPU PCB unit.</td>
<td></td>
</tr>
<tr>
<td>FDD or Disk CPU PCB (Gate Array #3)</td>
<td>Replace the FDD unit or disk. Replace the CPU PCB unit.</td>
<td></td>
</tr>
<tr>
<td>The disk is write-protected.</td>
<td>Remove the protect.</td>
<td></td>
</tr>
</tbody>
</table>
### 4-6. ROM Check Sum Table

<table>
<thead>
<tr>
<th>IC NO.</th>
<th>ROM NO.</th>
<th>ROM NAME &amp; VERSION NO.</th>
<th>CHECK SUM</th>
<th>DATE</th>
</tr>
</thead>
<tbody>
<tr>
<td>IC2</td>
<td>ROM0L</td>
<td>B91U01</td>
<td>$9F1F</td>
<td>6/22/87</td>
</tr>
<tr>
<td>IC2</td>
<td>ROM0L</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC2</td>
<td>ROM0L</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC2</td>
<td>ROM0L</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC4</td>
<td>ROM0H</td>
<td>B91U02</td>
<td>$FF0A</td>
<td>6/22/87</td>
</tr>
<tr>
<td>IC4</td>
<td>ROM0H</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC4</td>
<td>ROM0H</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC4</td>
<td>ROM0H</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC3</td>
<td>ROM1L</td>
<td>B91U03</td>
<td>$79BF</td>
<td>6/22/87</td>
</tr>
<tr>
<td>IC3</td>
<td>ROM1L</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC3</td>
<td>ROM1L</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC3</td>
<td>ROM1L</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC5</td>
<td>ROM1H</td>
<td>B91U04</td>
<td>$03FF</td>
<td>6/22/87</td>
</tr>
<tr>
<td>IC5</td>
<td>ROM1H</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC5</td>
<td>ROM1H</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
<tr>
<td>IC5</td>
<td>ROM1H</td>
<td></td>
<td>$</td>
<td>/ /</td>
</tr>
</tbody>
</table>

Table 4-1
<table>
<thead>
<tr>
<th>Section</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>How to Use Parts List</td>
<td>i ~ ii</td>
</tr>
<tr>
<td>A. Assembly Location Diagram</td>
<td>A-1</td>
</tr>
<tr>
<td>B. Assemblies</td>
<td>B-1 ~ B-2</td>
</tr>
<tr>
<td>C-1. CPU PCB Unit</td>
<td>C-1 ~ C-6</td>
</tr>
<tr>
<td>C-2. CRT/Power Supply Unit</td>
<td>C-7 ~ C-8</td>
</tr>
<tr>
<td>C-2. CRT/Power Supply Unit (MATSUSHITA)</td>
<td>C-11 ~ C-12</td>
</tr>
<tr>
<td>C-3. Power Supply Unit</td>
<td>C-9 ~ C-10</td>
</tr>
<tr>
<td>C-4. CRT/Power Supply Unit (GOLDSTAR)</td>
<td>C-13 ~ C-16</td>
</tr>
<tr>
<td>C-5. CRT/Power Supply Unit</td>
<td>C-17 ~ C-18</td>
</tr>
<tr>
<td>C-6. MICRO FDD Unit</td>
<td>C-19 ~ C-20</td>
</tr>
<tr>
<td>C-7. Keyboard Unit</td>
<td>C-21 ~ C-22</td>
</tr>
<tr>
<td>C-8. Keytops (USA/CANADA/ASIA/OCEANIA)</td>
<td>C-23 ~ C-24</td>
</tr>
<tr>
<td>C-9. Keytops (QUEBEC)</td>
<td>C-25 ~ C-26</td>
</tr>
<tr>
<td>C-10. Keytops (LATIN)</td>
<td>C-27 ~ C-28</td>
</tr>
<tr>
<td>C-12. Keytops (DENMARK)</td>
<td>C-29 ~ C-30</td>
</tr>
<tr>
<td>C-13. Keytops (SWEDEN/FINLAND)</td>
<td>C-31 ~ C-32</td>
</tr>
<tr>
<td>C-14. Keytops (NETHERLANDS)</td>
<td>C-33 ~ C-34</td>
</tr>
<tr>
<td>C-15. Keytops (W. GERMANY)</td>
<td>C-35 ~ C-36</td>
</tr>
<tr>
<td>C-16. Keytops (SWITZERLAND)</td>
<td>C-37 ~ C-38</td>
</tr>
<tr>
<td>C-17. Keytops (FRANCE)</td>
<td>C-39 ~ C-40</td>
</tr>
<tr>
<td>C-18. Keytops (U.K.)</td>
<td>C-41 ~ C-42</td>
</tr>
<tr>
<td>C-19. Keytops (SPAIN)</td>
<td>C-43 ~ C-44</td>
</tr>
<tr>
<td>C-20. Keytops (ITALY)</td>
<td>C-45 ~ C-46</td>
</tr>
<tr>
<td>C-22. Keytops (JAPAN)</td>
<td>C-49 ~ C-50</td>
</tr>
<tr>
<td>D. Screws</td>
<td>D,E-1</td>
</tr>
<tr>
<td>E. Tools</td>
<td>D,E-1</td>
</tr>
<tr>
<td>F. Numerical Index</td>
<td>F-1 ~ F-24</td>
</tr>
</tbody>
</table>
HOW TO USE PARTS LIST

The Parts List is classified as follows:
A. ASSEMBLY LOCATION DIAGRAM
B. ASSEMBLIES (Exploded View)
C. PARTS LOCATION
   DIAGRAM by FRU and ASSEMBLY
D. SCREWS
E. TOOLS
F. NUMERICAL INDEX

Pages of Parts Catalog indicate as follows:
XX – XX
   Serial Number of the same Fig. No.
   Fig. No.

ASSEMBLY LOCATION DIAGRAMS

These diagrams show the locations of major assemblies of the Typewriter and Figure No.

FINDING A PARTS NUMBER

Refer to the Assembly Location Diagram and find the Figure Number of the assembly of interest. Turn to the page(s), locate the parts on the exploded view, and find its Key Number. Refer to the Parts List on the page facing the exploded view and find the Key Number. Parts Number and Quantity required for your type of machine. Because the parts commonly used for all products such as screw, nut, washer, retaining ring and pin are indicated on the illustrations by 3-digit key numbers which consist of one alphabetical digit and two numerical digits, the Parts numbers should be referred to D. SCREWS.

PARTS LIST PAGES

The Parts List pages contain the following columns and information.

(1) Figure and Key Number Column
   The first column shows the Figure Number of the illustration corresponding to the Parts List, and the Key Number that identifies the parts on the illustration.

(2) Parts Number Column
   The second column shows the Parts Number for the parts. This Number must be used when ordering replacement parts or assemblies.

(3) Rank Column
   Parts marked “N” are service parts, but are not stocked items. They are produced on a special-order basis. Parts marked “X” are not service parts, therefore they are not available.

(4) Quantity Column
   The quantity shown in this column is the number of parts used in the figure. However, the quantity listed for an assembly indicates the number of that assembly per machine.

   The letters in this column indicate as follows:
   RF............ Parts listed up for reference
   AR............ Parts of which quantity can not be specified or used according to necessary quantity

(5) Description Column
   The description column lists the parts names in English that should be used when ordering the parts. Major specifications and model numbers of electrical parts are sometimes described at the end of the name.

(6) Remarks Column
   The parts marked • are never used before and they are newly stocked as service parts.

(7) Country Code Column
   The country code lists available parts by country of origin.
   When ordering the parts, refer to the code.
SCREWS

This table covers all the parts (including options) commonly used for all products such as screw, nut, washer, retaining ring and pin. They are indicated in a three-digit form on the illustration with an alphabetical character at the head, different from general key number.

Be careful that these parts are not included in the parts list corresponding to the illustrations.

NUMERICAL INDEX

There is a Numerical Index at the end of this catalog. It lists in numerical order every Parts Number contained in the Parts List.

In the left section of the Numerical Index Table are Parts Number, and Figure Number and Key Number are shown in the right section.

Be careful that the Index does not include the parts numbers listed in the Screw Table.
A. ASSEMBLY LOCATION DIAGRAM

- CRT/POWER SUPPLY UNIT
  - 02 03 04 05
- MICRO FDD UNIT
  - C-6
- CPU PCB UNIT
  - C-1
- KEYBOARD UNIT
  - C-7 C-22
### B. ASSEMBLIES

**[COUNTRY CODE AS SHOWN BELOW]**

<table>
<thead>
<tr>
<th>FIGURE &amp; KEY NO.</th>
<th>PART NUMBER</th>
<th>Q'TY</th>
<th>DESCRIPTION</th>
<th>REMARKS</th>
</tr>
</thead>
<tbody>
<tr>
<td>B 1</td>
<td>NA1-0880-000</td>
<td>1</td>
<td>NOB, VARIABLE RESISTOR</td>
<td>ABCDEFHJLMNOPQR...</td>
</tr>
<tr>
<td>B 2</td>
<td>NA1-3834-000</td>
<td>2</td>
<td>RUBBER FOOT</td>
<td>*</td>
</tr>
<tr>
<td>B 3</td>
<td>NA1-5152-000</td>
<td>1</td>
<td>BASE CASE</td>
<td>*</td>
</tr>
<tr>
<td>B 4</td>
<td>N51-1103-000</td>
<td>1</td>
<td>KEYBOARD COVER ASSEMBLY</td>
<td>*</td>
</tr>
<tr>
<td>B 5</td>
<td>NA1-5154-000</td>
<td>1</td>
<td>ESCUTCHEON</td>
<td>*</td>
</tr>
<tr>
<td>B 6</td>
<td>NA1-5156-000</td>
<td>1</td>
<td>CRT COVER</td>
<td>*</td>
</tr>
<tr>
<td>B 7</td>
<td>NA1-5156-000</td>
<td>1</td>
<td>BASE PLATE</td>
<td>*</td>
</tr>
<tr>
<td>B 8</td>
<td>NA1-5157-000</td>
<td>2</td>
<td>EARTH PLATE A</td>
<td>*</td>
</tr>
<tr>
<td>B 9</td>
<td>NA1-5158-000</td>
<td>2</td>
<td>EARTH PLATE B</td>
<td>*</td>
</tr>
<tr>
<td>B 10</td>
<td>NA1-5159-000</td>
<td>1</td>
<td>MOUNTING PLATE, FDD</td>
<td>*</td>
</tr>
<tr>
<td>B 11</td>
<td>NA1-5160-000</td>
<td>1</td>
<td>SHIELD PLATE, CRT</td>
<td>*</td>
</tr>
<tr>
<td>B 12</td>
<td>NA9-0766-000</td>
<td>1</td>
<td>PLATE NUT</td>
<td>*</td>
</tr>
<tr>
<td>B 13</td>
<td>NG3-1030-000</td>
<td>N</td>
<td>CPU PCB UNIT</td>
<td>*FRU A...</td>
</tr>
<tr>
<td>B 14</td>
<td>NG3-1031-000</td>
<td>N</td>
<td>CPU PCB UNIT</td>
<td>*FRU D...</td>
</tr>
<tr>
<td>B 15</td>
<td>NG3-1032-000</td>
<td>N</td>
<td>CPU PCB UNIT</td>
<td>*FRU C...</td>
</tr>
<tr>
<td>B 16</td>
<td>NG3-1033-000</td>
<td>N</td>
<td>CPU PCB UNIT</td>
<td>*FRU M...</td>
</tr>
<tr>
<td>B 17</td>
<td>NG3-1034-000</td>
<td>N</td>
<td>CPU PCB UNIT</td>
<td>*FRU J...</td>
</tr>
<tr>
<td>B 18</td>
<td>NG3-1035-000</td>
<td>N</td>
<td>CPU PCB UNIT</td>
<td>*FRU G...</td>
</tr>
<tr>
<td>B 19</td>
<td>NS5-0652-000</td>
<td>N</td>
<td>CRT/POWER SUPPLY UNIT MATS</td>
<td>*FRU A...</td>
</tr>
<tr>
<td>B 20</td>
<td>NS5-0651-000</td>
<td>N</td>
<td>CRT/POWER SUPPLY UNIT GOLDS</td>
<td>*FRU A...</td>
</tr>
<tr>
<td>B 21</td>
<td>NS5-0633-000</td>
<td>N</td>
<td>CRT/POWER SUPPLY UNIT GOLDS</td>
<td>*FRU A...</td>
</tr>
<tr>
<td>B 22</td>
<td>NS5-0653-000</td>
<td>N</td>
<td>MICRO FDD UNIT</td>
<td>*FRU A...</td>
</tr>
<tr>
<td>B 23</td>
<td>NS5-0686-000</td>
<td>N</td>
<td>KEYBOARD UNIT</td>
<td>*FRU ABC...</td>
</tr>
<tr>
<td>B 24</td>
<td>NS5-0689-000</td>
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<td>KEYBOARD UNIT</td>
<td>*FRU D...</td>
</tr>
<tr>
<td>B 25</td>
<td>NS5-0690-000</td>
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<td>KEYBOARD UNIT</td>
<td>*FRU E...</td>
</tr>
<tr>
<td>B 26</td>
<td>NS5-0691-000</td>
<td>N</td>
<td>KEYBOARD UNIT</td>
<td>*FRU F...</td>
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<tr>
<td>B 27</td>
<td>NS5-0692-000</td>
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<td>KEYBOARD UNIT</td>
<td>*FRU G...</td>
</tr>
<tr>
<td>B 28</td>
<td>NS5-0693-000</td>
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<td>KEYBOARD UNIT</td>
<td>*FRU H...</td>
</tr>
<tr>
<td>B 29</td>
<td>NS5-0694-000</td>
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<td>KEYBOARD UNIT</td>
<td>*FRU I...</td>
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<tr>
<td>B 30</td>
<td>NS5-0695-000</td>
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<td>KEYBOARD UNIT</td>
<td>*FRU J...</td>
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<tr>
<td>B 31</td>
<td>NS5-0696-000</td>
<td>N</td>
<td>KEYBOARD UNIT</td>
<td>*FRU K...</td>
</tr>
<tr>
<td>B 32</td>
<td>NS5-0697-000</td>
<td>N</td>
<td>KEYBOARD UNIT</td>
<td>*FRU L...</td>
</tr>
<tr>
<td>B 33</td>
<td>NS5-0698-000</td>
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<td>KEYBOARD UNIT</td>
<td>*FRU M...</td>
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<tr>
<td>B 34</td>
<td>NS5-0699-000</td>
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<td>KEYBOARD UNIT</td>
<td>*FRU N...</td>
</tr>
<tr>
<td>B 35</td>
<td>NS5-0700-000</td>
<td>N</td>
<td>KEYBOARD UNIT</td>
<td>*FRU N...</td>
</tr>
<tr>
<td>B 36</td>
<td>NS5-0701-000</td>
<td>N</td>
<td>KEYBOARD UNIT</td>
<td>*FRU O...</td>
</tr>
<tr>
<td>B 37</td>
<td>NS5-0702-000</td>
<td>N</td>
<td>KEYBOARD UNIT</td>
<td>*FRU R...</td>
</tr>
<tr>
<td>B 38</td>
<td>WM1-0013-000</td>
<td>X</td>
<td>LITHIUM BATTERY CR-2032</td>
<td>ABCDEFHJLMNOPQR...</td>
</tr>
<tr>
<td>B 39</td>
<td>WT3-0027-000</td>
<td>1</td>
<td>POWER SUPPLY CORD 120V</td>
<td>*A...D...</td>
</tr>
<tr>
<td>B 40</td>
<td>WT3-0028-000</td>
<td>1</td>
<td>POWER SUPPLY CORD 240V</td>
<td>*GHIJKL...</td>
</tr>
<tr>
<td>B 41</td>
<td>WT3-0029-000</td>
<td>1</td>
<td>POWER SUPPLY CORD 240V</td>
<td>*C...D...</td>
</tr>
<tr>
<td>B 42</td>
<td>WT3-0030-000</td>
<td>1</td>
<td>POWER SUPPLY CORD 100,115V</td>
<td>*B...EF...R...</td>
</tr>
<tr>
<td>B 43</td>
<td>WT3-0044-000</td>
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<td>*N...K...</td>
</tr>
<tr>
<td>B 44</td>
<td>NA1-5823-000</td>
<td>2</td>
<td>SPRING, EARTH</td>
<td>*A...B...</td>
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<tr>
<td>B 45</td>
<td>NA1-5820-000</td>
<td>1</td>
<td>SHIELD PLATE, KEYBOARD</td>
<td>*A...B...</td>
</tr>
<tr>
<td>B 46</td>
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<td>1</td>
<td>SHIELD PLATE, CPU PCB</td>
<td>*A...B...</td>
</tr>
<tr>
<td>B 47</td>
<td>HH2-1268-000</td>
<td>1</td>
<td>Modular cord</td>
<td>*ABC...R...</td>
</tr>
</tbody>
</table>
C-1. CPU PCB UNIT
**C-1. CPU PCB UNIT**

[COUNTRY CODE AS SHOWN BELOW]

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### C-1. CPU PCB UNIT

[ COUNTRY CODE AS SHOWN BELOW ]

**COUNTRY CODE:**
- USA/CANADA: A
- LATIN (115V): B
- ASIA: C
- LATIN (230V): D
- SWEDEN/FINLAND: E
- NETHERLANDS: F
- SWITZERLAND: G
- ITALY: H
- SWEDEN/FINLAND: I
- UK: J
- JAPAN: K
- NETHERLANDS: L
- SWITZERLAND: M
- CANADA: N
- S. AFRICA: O
- JAPAN: P
- UK: Q
- S. AFRICA: R
- CANADA: S
- ITALY: T
- UK: U
- JAPAN: V
- SWITZERLAND: W
- S. AFRICA: X
- CANADA: Y
- USA/CANADA: Z

#### FIGURE KEY NO. | PART NUMBER | Q'TY | DESCRIPTION | REMARKS
--- | --- | --- | --- | ---
C-1 57 | VC5-9870-474 | 1 | CERAMIC CAP. 470000PF -45/-85 | A...D...R...PNOPQ...
C-1 58 | VC5-1970-471 | 1 | CERAMIC CAP. 4700PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 59 | VC5-1910-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 60 | WA8-0135-000 | 1 | VARISTOR MFC 125AX333M | A...D...R...PNOPQ...
C-1 62 | VC3-1428-000 | 6 | CERAMIC CAP. 0.1UF 25V -25/-85 | BC.EFGHJKLNPQ...
C-1 67 | WA8-0135-000 | 6 | VARISTOR MFC 125AX333M | A...D...R...PNOPQ...
C-1 68 | VC5-1589-222 | 1 | CERAMIC CAP. 2200PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 70 | VC5-1700-350 | 1 | CERAMIC CAP. 20PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 71 | VC5-1700-200 | 1 | CERAMIC CAP. 20PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 72 | VC1-2251-107 | 1 | ALUMINUM CAP. 10UF 25V | A...D...R...PNOPQ...
C-1 73 | VC1-2251-107 | 1 | ALUMINUM CAP. 10UF 25V | A...D...R...PNOPQ...
C-1 76 | VC1-2101-105 | 1 | ALUMINUM CAP. 1UF 10V | A...D...R...PNOPQ...
C-1 77 | VC1-2101-105 | 1 | ALUMINUM CAP. 1UF 10V | A...D...R...PNOPQ...
C-1 78 | VC1-2101-476 | 1 | ALUMINUM CAP. 4.7UF 16V | A...D...R...PNOPQ...
C-1 79 | VC1-2101-107 | 1 | ALUMINUM CAP. 100UF 16V | A...D...R...PNOPQ...
C-1 80 | VC1-2251-476 | 1 | ALUMINUM CAP. 47UF 25V | A...D...R...PNOPQ...
C-1 81 | WA8-0135-000 | 1 | VARISTOR MFC 125AX333M | A...D...R...PNOPQ...
C-1 82 | VC1-2255-476 | 1 | ALUMINUM CAP. 47UF 25V | A...D...R...PNOPQ...
C-1 83 | WA8-0135-000 | 1 | VARISTOR MFC 125AX333M | A...D...R...PNOPQ...
C-1 84 | VC5-2500-336 | 1 | ALUMINUM CAP. 33UF 16V | A...D...R...PNOPQ...
C-1 85 | VC5-1710-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 86 | VC1-2101-105 | 1 | ALUMINUM CAP. 1UF 10V | A...D...R...PNOPQ...
C-1 87 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 88 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 89 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 90 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 91 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 92 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 93 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 94 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 95 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 96 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
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C-1 99 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 100 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 101 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 102 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 103 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 104 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 105 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 106 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
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C-1 109 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 110 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
C-1 111 | VC5-1700-102 | 1 | CERAMIC CAP. 100PF 50V -25/-85 | A...D...R...PNOPQ...
### C-1. CPU PCB UNIT

#### COUNTRY CODE AS SHOWN BELOW

|-------------------------------------|--------------|--------|------------|-----------|---------------|---------------|-----------|------------|-------------------|---------------|-------------|---------------|-----------|--------|--------------|---------|----------------|

#### FIGURE & KEY NO. PART NUMBER RANK Q'TY DESCRIPTION REMARKS COUNTRY CODE

| C-1 FB 1   | WE8-0012-000 | 23 | DATA LINE FILTER | .BC.EFGHIJKLMNOPQRSTUVWXYZ... |
| C-1 FB 23  | WE8-0060-000 | 15 | FERRITE BEAD     | A..D..................R... |
| C-1 FB 17  | WE8-0060-000 | 8  | FERRITE BEAD     | A..D..................R... |

| C-1 IC 1   | WA3-1847-000 | 1  | MOS LSI MOS60009P(CPU) | A..D..................R... |
| C-1 IC 2   | NH7-0597-000 | 1  | MOS LSI MOS780512-25(8911M+) | A..D..................R... |

| C-1 IC 2   | WA9-0598-000 | 1  | IC SOCKET 28PIN   | A..D..................R... |
| C-1 IC 3   | NH7-0599-000 | 1  | MOS LSI MOS780512-25(893U+M) | A..D..................R... |

| C-1 IC 3   | WA9-0598-000 | 1  | IC SOCKET 28PIN   | A..D..................R... |
| C-1 IC 4   | NH7-0598-000 | 1  | MOS LSI MOS780512-25(892U+M) | A..D..................R... |

| C-1 IC 5   | WA9-0598-000 | 1  | IC SOCKET 28PIN   | A..D..................R... |
| C-1 IC 6   | NH7-0584-000 | 1  | MOS LSI HN62301A-C11(MASK ROM) | A..D..................R... |

| C-1 IC 6   | NH7-0724-000 | 1  | MOS LSI HN62301APC38 | A..D..................R... |
| C-1 IC 6   | NH7-0813-000 | 1  | MOS LSI HN62301AP028 | A..D..................R... |

| C-1 IC 6   | NH7-1019-000 | 1  | MOS LSI MB831000-15P-263 | A..D..................R... |
| C-1 IC 7   | NH7-0814-000 | 1  | MOS LSI HN62301AP029 | A..D..................R... |

| C-1 IC 7   | WA9-0558-000 | 1  | IC SOCKET 28PIN   | A..D..................R... |
| C-1 IC 8   | NH7-1020-000 | 1  | MOS LSI MB831000-15P-264 | A..D..................R... |

| C-1 IC 8   | WA9-0558-000 | 1  | IC SOCKET 28PIN   | A..D..................R... |
| C-1 IC 9   | WA9-0558-000 | 1  | IC SOCKET 28PIN   | A..D..................R... |

| C-1 IC 10  | WA3-3177-000 | 1  | MOS LSI UPD434-155 (RAM) | A..D..................R... |
| C-1 IC 11  | WA3-0434-000 | 1  | TTL IC SN74128N | A..D..................R... |

| C-1 IC 12  | WA3-5002-000 | 1  | MOS LSI UPD65012 CW-191 | A..D..................R... |
| C-1 IC 13  | WA9-0067-000 | 1  | IC SOCKET BP     | A..D..................R... |

| C-1 IC 14  | WA9-0067-000 | 1  | IC SOCKET BP     | A..D..................R... |
| C-1 IC 15  | WA9-0067-000 | 1  | IC SOCKET BP     | A..D..................R... |

| C-1 IC 16  | WA3-3023-000 | 1  | MOS LSI MB81464-15 (RAM) | A..D..................R... |

| C-1 IC 17  | NH4-5001-000 | 1  | MOS LSI UPD65013 CW-276 | A..D..................R... |
| C-1 IC 18  | NH4-5003-000 | 1  | MOS LSI UPD65012 CW-141 | A..D..................R... |

| C-1 IC 19  | WA3-0894-000 | 1  | TTL IC H074L095P | A..D..................R... |
| C-1 IC 20  | WA3-1197-000 | 1  | TTL IC H074L132P | A..D..................R... |

| C-1 IC 21  | WA3-3048-000 | 1  | MOS LSI MB886101(DQUART) | A..D..................R... |
| C-1 IC 22  | WA4-0722-000 | 1  | IC U1A1488FC | A..D..................R... |

| C-1 IC 23  | WA4-0723-000 | 1  | IC U1A1488APC | A..D..................R... |
| C-1 IC 24  | WA4-0770-000 | 1  | MOS LSI S52213 | A..D..................R... |

| C-1 IC 25  | WA4-0709-000 | 1  | MOS LSI S52212A | A..D..................R... |
| C-1 IC 26  | WA4-0808-000 | 1  | IC NFC 4558C | A..D..................R... |
## C-1. CPU PCB UNIT

[COUNTRY CODE AS SHOWN BELOW]

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C-2. CRT/POWER SUPPLY UNIT (MATSUSHITA)
C-2. CRT/POWER SUPPLY UNIT
[ COUNTRY CODE AS SHOWN BELOW ]

COUNTRY CODE:
A: USA/Canada  E: Latin(115V)  I: Sweden/Finland  M: France  Q: S.Africa
C: Oceania  G: Norway  K: W.Germany  O: Spain
D: Quebec  H: Denmark  L: Switzerland  P: Italy

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C-8
## C-3. POWER SUPPLY UNIT

![Diagram of C-3. POWER SUPPLY UNIT](image)

This power supply is covered by U.S. Patent: 4,323,961

1980 ASTEC COMPONENTS LTD

**WARNING:** FOR CONTINUED PROTECTION AGAINST FIRE HAZARD, REPLACE ONLY WITH SAME TYPE AND RATING OF FUSE F2A250V.

### C-3. POWER SUPPLY UNIT

[Country Code as shown below]

**Country Code:**
- **A**: USA/CANADA
- **B**: ASIA
- **C**: OCEANIA
- **D**: QUEBEC
- **E**: LATIN (115V)
- **F**: LATIN (230V)
- **G**: NORWAY
- **H**: DENMARK
- **I**: SWEDEN/FINLAND
- **J**: NETHERLAND
- **K**: W. GERMANY
- **L**: SWITZERLAND
- **M**: FRANCE
- **N**: U.K.
- **O**: SPAIN
- **P**: ITALY
- **Q**: S.AFRICA
- **R**: JAPAN

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## Table of Contents

- **C-3. Power Supply Unit**

### Country Code

- **A**: USA/Canada
- **B**: Asia
- **C**: Oceania
- **D**: British Isles

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C-4. CRT/POWER SUPPLY UNIT (GOLDSTAR)
**C-4. CRT/POWER SUPPLY UNIT (GOLDSTAR)**

[ COUNTRY CODE AS SHOWN BELOW ]

COUNTRY CODE:

A: USA/Canada  E: Latin(115V)  I: Sweden/Finland  M: France  Q: S.Africa
C: Oceania  G: Norway  K: W.Germany  Q: Spain
D: Quebec  H: Denmark  L: Switzerland  P: Italy

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C-5. CRT/POWER SUPPLY PCB ASSEMBLY
### C-5. CRT/POWER SUPPLY PCB ASSEMBLY

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**NOTE:**
- The table provides the part number, rank, quantity, description, and country code for various components of the CRT/Power Supply PCB assembly.
- The table includes descriptions such as "ALUMINUM CAP," "FILM CAP," and "CERAMIC CAP."
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C-6. MICRO FDD UNIT
### C-6 MICRO FDD UNIT

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#### FIGURE & KEY NO. | PART NUMBER | Q'TY | DESCRIPTION | REMARKS |
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* indicates parts that require special handling or instructions.
C-7. KEYBOARD UNIT
# C-7. Keyboard Unit

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C-8. KEYTOPS (U.S.A./CANADA/ASIA/OCEANIA)
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### C-11. KEYTOPS (NORWAY)

#### Country Code as Shown Below

**Country Code:**
- A: USA/Canada
- B: Asia
- C: Oceania
- D: Quebec
- E: Latin (11s)
- F: Latin (230v)
- G: Norway
- H: Denmark
- I: Sweden/Finland
- J: Netherlands
- K: West Germany
- L: Switzerland
- M: France
- N: U.K.
- O: Spain
- P: Italy

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C-12. KEYTOPS (DENMARK)
### C-12. KEYPADS (DENMARK)

**COUNTRY CODE AS SHOWN BELOW**

**COUNTRY CODE:**
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- B: Asia
- C: Oceania
- D: Quebec
- E: Latin(115V)
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C-13. KEYTOPS (SWEDEN/FINLAND)
## C-13. KEYPADS (SWEDEN/FINLAND)

[**COUNTRY CODE AS SHOWN BELOW**]

**COUNTRY CODE:**
- **USA/Canada:** A
- **Asia:** B
- **Europe:** C
- **Oceania:** D
- **North America:** E
- **UK:** F
- **Netherlands:** J
- **Western Europe:** K
- **Spain:** Q
- **South Africa:** R

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### C-14. KEYTOPS (NETHERLANDS)

**[COUNTRY CODE AS SHOWN BELOW]**

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- A: USA/CANADA  E: LATIN (15SV)
- B: ASIA  F: LATIN (23SV)
- C: OCEANIA  G: NORWAY
- D: QUEBEC  H: DENMARK  I: SWEDEN/FINLAND
- M: FRANCE  N: U.K.
- Q: S.AFRICA  R: JAPAN
- K: W.GERMANY  O: SPAIN
- L: SWITZERLAND  P: ITALY

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C-15. KEYTOPS (W. GERMANY)

COUNTRY CODE:

C: OCEANIA G: NORWAY K: W.GERMANY O: SPAIN
D: QUEBEC H: DENMARK L: SWITZERLAND P: ITALY
C-16. KEYTOPS (SWITZERLAND)
### Figure C-16

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C-17. KEYTOPS (FRANCE)
### C-17. KEYTOPS (FRANCE)

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### C-18. KEYTOPS (U.K.)

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C-19 KEYTOPS (SPAIN)
C-20. KEYTOPS (ITALY)

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C-21. KEYPADS (S.AFRICA)
**C-21. KEYPOPS (S. AFRICA)**

[COUNTRY CODE AS SHOWN BELOW]

**COUNTRY CODE:**

- A: USA/Canada
- B: Latin (115v)
- C: Oceania
- D: Quebec
- E: Latin (230v)
- F: Sweden/Finnland
- G: Norway
- H: Denmark
- I: Sweden/Finnland
- J: Netherlands
- K: W. Germany
- L: Switzerland
- M: France
- N: U.K.
- O: Spain
- P: Italy

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B: Asia  F: Latin(Europe)  J: Netherlands  N: U.K.  R: Japan
C: Oceania  G: Norway  K: W. Germany  O: Spain
D: Quebec  H: Denmark  L: Switzerland  P: Italy

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### D. SCREWS

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### E. TOOLS

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- C: Oceania  
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- E: Latin America (Canada)  
- F: Latin America (Outside Canada)  
- G: Norway  
- H: Denmark  
- I: Sweden/Finland  
- J: Netherlands  
- K: West Germany  
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- M: France  
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- E: LATIN(15V)
- F: LATIN(230V)
- G: NORWAY
- H: DENMARK
- I: SWEDEN/FINLAND
- J: NETHERLANDS
- K: W.GERMANY
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- N: U.K.
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### FIGURE NUMERICAL INDEX

[Country code as shown below]

**Country Code:**
- A: USA/Canada
- B: Australia
- C: Oceania
- D: Quebec
- E: Latin (1590)
- F: Latin (230V)
- G: Norway
- H: Denmark
- I: Sweden/Finland
- J: Netherlands
- K: W. Germany
- L: Switzerland
- M: France
- N: U.K.
- Q: S. Africa
- R: Japan
- S: Asia
- T: USA/CANADA
- U: LA (115V)
- V: LA (230V)
- W: France
- X: Germany
- Y: Italy
- Z: Rest of World

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- NY7-4827-000
- NY7-4829-000
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**Remarks:**
- A: KEYTOP
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- D: KEYTOP
- E: KEYTOP
- F: KEYTOP
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- Y: KEYTOP
- Z: KEYTOP
### F. NUMERICAL INDEX

[ COUNTRY CODE AS SHOWN BELOW ]

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- A: USA/CANADA
- B: ASIA
- C: OCEANIA
- D: QUEBEC
- E: LATIN(115V)
- F: LATIN(230V)
- G: NETHERLANDS
- H: DENMARK
- I: SWEDEN/FINLAND
- J: N. U.K.
- K: N. GERMANY
- L: SWITZERLAND
- M: FRANCE
- N: S. AFRICA
- O: JAPAN
- P: ITALY

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[COUNTRY CODE AS SHOWN BELOW]

**COUNTRY CODE:**

C: OCEANIA  G: NORWAY  K: W.GERMANY  O: SPAIN
D: QUEBEC  H: DENMARK  L: SWITZERLAND  P: ITALY

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## F. Numerical Index

[COUNTRY CODE AS SHOWN BELOW]

### Country Codes:
- **A:** USA/Canada
- **B:** Asia
- **C:** Oceania
- **D:** Quebec
- **E:** Latin (115V)
- **F:** Latin (230V)
- **G:** Norway
- **H:** Denmark
- **I:** Sweden/Finnland
- **J:** Netherlands
- **K:** W. Germany
- **L:** Switzerland
- **M:** France
- **N:** U.K.
- **O:** Japan
- **P:** Italy
- **Q:** S. Africa

### Figure & Key No. | Part Number | Rank | Q'Ty | Description | Remarks | Country Code
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### F. Numerical Index

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### F. NUMERICAL INDEX

#### COUNTRY CODE:

- **A**: USA/Canada
- **B**: Asia
- **C**: Oceania
- **D**: Quebec
- **E**: Latin(115V)
- **F**: Latin(230V)
- **G**: Norway
- **H**: Denmark
- **I**: Sweden/Finnland
- **J**: Netherlands
- **K**: W. Germany
- **L**: Switzerland
- **M**: France
- **N**: U.K.
- **O**: Spain
- **P**: Italy
- **Q**: S. Africa

#### FIGURE & KEY NO. | PART NUMBER | RANK | Q'TY | DESCRIPTION | REMARKS |
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### F. NUMERICAL INDEX

**[ COUNTRY CODE AS SHOWN BELOW ]**

**COUNTRY CODE:**
- A: USA/CANADA
- B: ASIA
- C: OCEANIA
- D: QUEBEC
- E: SWEDEN/FINLAND
- F: NETHERLANDS
- G: NORWAY
- H: DENMARK
- J: NETHERLANDS
- K: W. GERMANY
- L: SWITZERLAND
- M: FRANCE
- N: U.K.
- O: SPAINT
- P: ITALY
- Q: S.AFRICA

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- B: Asia  F: Latin(200v)  J: Netherlands  N: U.K.  R: Japan
- C: Oceania  G: Norway  K: West Germany  Q: Spain
- D: Quebec  H: Denmark  L: Switzerland  P: Italy

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